



**Alfa-MOS  
Technology**

**AFC1036E  
30V N & P Pair  
Enhancement Mode MOSFET**

## General Description

AFC1036E, N & P Pair enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent  $R_{DS(ON)}$ , low gate charge. These devices are particularly suited for low voltage power management, such as smart phone and notebook computer and other battery powered circuits, and low in-line power loss are needed in commercial industrial surface mount applications.

## Features

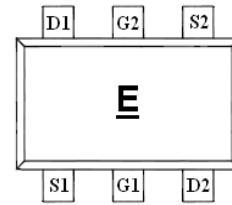
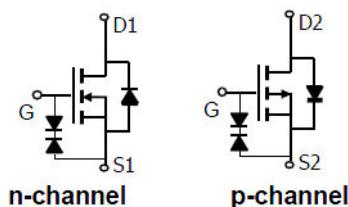
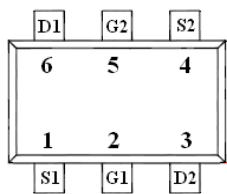
### N-Channel

- 30V/0.6A,  $R_{DS(ON)}=500m\Omega$  @  $V_{GS}=4.5V$
- 30V/0.5A,  $R_{DS(ON)}=700m\Omega$  @  $V_{GS}=2.5V$

### P-Channel

- -30V/-0.4A,  $R_{DS(ON)}=900m\Omega$  @  $V_{GS}=-4.5V$
- -30V/-0.3A,  $R_{DS(ON)}=1400 m\Omega$  @  $V_{GS}=-2.5V$
- Low Offset (Error) Voltage
- Low-Voltage Operation
- High-Speed Circuits
- Low Battery Voltage Operation
- ESD Protected
- SOT-563 package design

## Pin Description ( SOT-563 )



## Application

- Load Switch for Portable Devices, Smart Phones, Pagers

## Pin Define

Pin	Symbol	Description
1	S1	Source 1
2	G1	Gate 1
3	D2	Drain 2
4	S2	Source 2
5	G2	Gate 2
6	D1	Drain1

## Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFC1036ES56RG	E	SOT-563	Tape & Reel	3000 EA

※ E parts code

※ AFC1036ES56RG : 7" Tape & Reel ; Pb- Free ; Halogen -Free



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### Absolute Maximum Ratings

( $T_A=25^\circ\text{C}$  Unless otherwise noted)

Parameter	Symbol	Value		Unit	
		N-Channel	P-Channel		
Drain-Source Voltage	$V_{DSS}$	30	-30	V	
Gate -Source Voltage	$V_{GSS}$	$\pm 12$	$\pm 12$	V	
Continuous Drain Current( $T_J=150^\circ\text{C}$ )	$I_D$	0.6	-0.4	A	
		0.4	-0.2		
Pulsed Drain Current	$I_{DM}$	2	-1	A	
Continuous Source Current(Diode Conduction)	$I_S$	0.2	-0.2	A	
Power Dissipation	$P_D$	0.3	0.2	W	
		0.2			
Operating Junction Temperature	$T_J$	-55/150		°C	
Storage Temperature Range	$T_{STG}$	-55/150		°C	

### Electrical Characteristics ( N-Channel )

( $T_A=25^\circ\text{C}$  Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	30			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	0.5		1.5	
Gate Leakage Current	$I_{GSS}$	$V_{DS}=0\text{V}, V_{GS}=\pm 12\text{V}$			$\pm 5$	mA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=24\text{V}, V_{GS}=0\text{V}$			1	
		$V_{DS}=24\text{V}, V_{GS}=0\text{V}$ $T_J=85^\circ\text{C}$			5	uA
On-State Drain Current	$I_{D(\text{on})}$	$V_{DS} \geq 5\text{V}, V_{GS}=4.5\text{V}$	0.6			A
Drain-Source On-Resistance	$R_{DS(\text{on})}$	$V_{GS}=4.5\text{V}, I_D=0.6\text{A}$		350	500	$\text{m}\Omega$
		$V_{GS}=2.5\text{V}, I_D=0.5\text{A}$		600	700	
Forward Transconductance	$g_{FS}$	$V_{DS}=15\text{V}, I_D=0.6\text{A}$		1		S
Diode Forward Voltage	$V_{SD}$	$I_S=0.15\text{A}, V_{GS}=0\text{V}$		0.65	1.2	V
<b>Dynamic</b>						
Input Capacitance	$C_{iss}$	$V_{DS}=15\text{V}, V_{GS}=0\text{V}$ $f=1\text{MHz}$		28		pF
Output Capacitance	$C_{oss}$			10		
Reverse Transfer Capacitance	$C_{rss}$			6		
Total Gate Charge	$Q_g$	$V_{DS}=15\text{V}, V_{GS}=4.5\text{V}$ $I_D=0.6\text{A}$		0.6	1.2	nC
Gate-Source Charge	$Q_{gs}$			0.2		
Gate-Drain Charge	$Q_{gd}$			0.2		
Turn-On Time	$t_{d(\text{on})}$	$V_{DD}=15\text{V}, R_L=30\Omega$ $I_D=0.5\text{A}, V_{GEN}=4.5\text{V}$ $R_G=1\Omega$		25	40	ns
	$t_r$			25	40	
Turn-Off Time	$t_{d(\text{off})}$			15	25	
	$t_f$			15	25	



### Electrical Characteristics ( P-Channel )

( $T_A=25^\circ\text{C}$  Unless otherwise noted)

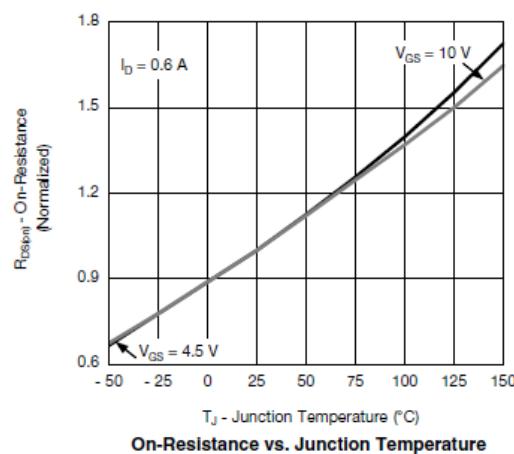
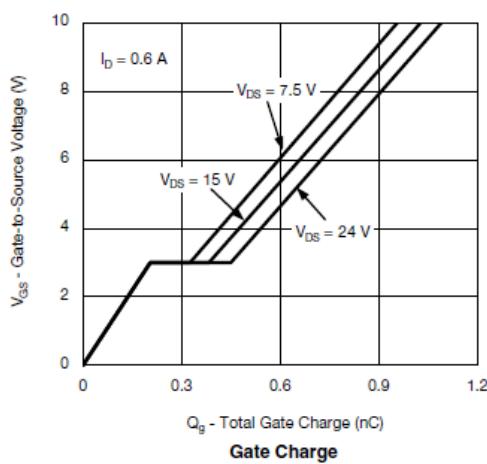
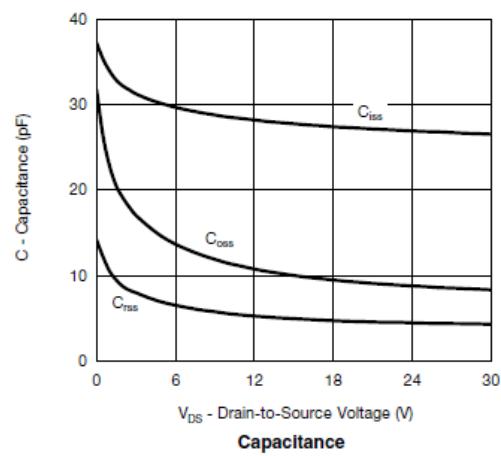
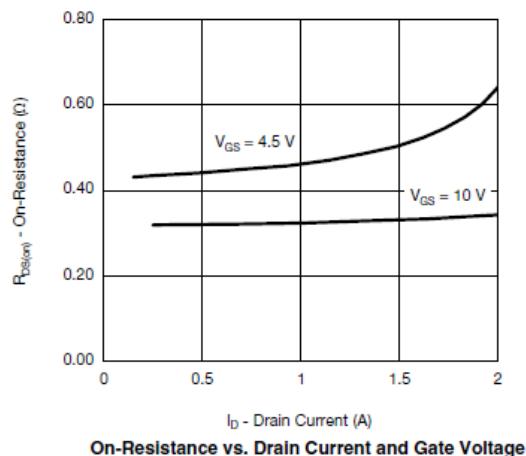
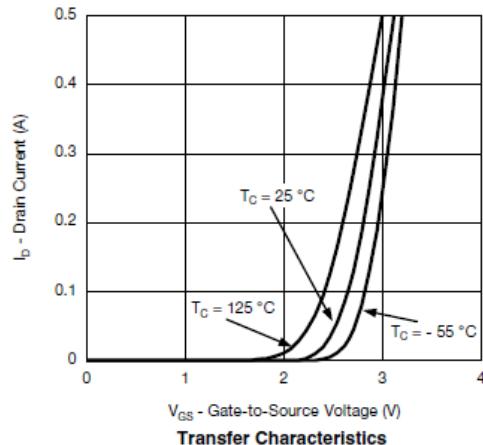
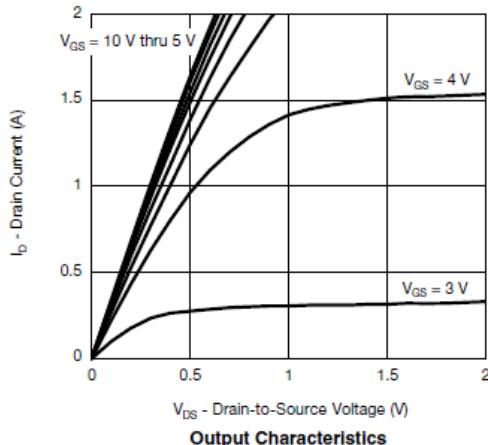
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-30			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-0.7		-1.5	
Gate Leakage Current	$I_{\text{GSS}}$	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm 12\text{V}$			$\pm 5$	mA
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=-24\text{V}, V_{\text{GS}}=0\text{V}$			-1	
		$V_{\text{DS}}=-24\text{V}, V_{\text{GS}}=0\text{V}$ $T_J=85^\circ\text{C}$			-5	uA
On-State Drain Current	$I_{\text{D}(\text{on})}$	$V_{\text{DS}} \geq 5\text{V}, V_{\text{GS}}=4.5\text{V}$	0.5			A
Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=0.4\text{A}$		450	900	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V}, I_{\text{D}}=0.3\text{A}$		1000	1400	
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}}=-15\text{V}, I_{\text{D}}=-0.5\text{A}$		1		S
Diode Forward Voltage	$V_{\text{SD}}$	$I_{\text{S}}=-0.3\text{A}, V_{\text{GS}}=0\text{V}$		0.65	1.2	V
<b>Dynamic</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=0\text{V}$ $f=1\text{MHz}$		34		pF
Output Capacitance	$C_{\text{oss}}$			12		
Reverse Transfer Capacitance	$C_{\text{rss}}$			8		
Total Gate Charge	$Q_g$	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=-4.5\text{V}$ $I_{\text{D}}=-0.4\text{A}$		0.8	1.3	nC
Gate-Source Charge	$Q_{\text{gs}}$			0.4		
Gate-Drain Charge	$Q_{\text{gd}}$			0.4		
Turn-On Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=-15\text{V}, R_{\text{L}}=38\Omega$ $I_{\text{D}}=-0.2\text{A}, V_{\text{GEN}}=-4.5\text{V}$ $R_{\text{G}}=1\Omega$		35	50	ns
	$t_r$			20	30	
Turn-Off Time	$t_{\text{d}(\text{off})}$			10	20	
	$t_f$			10	20	



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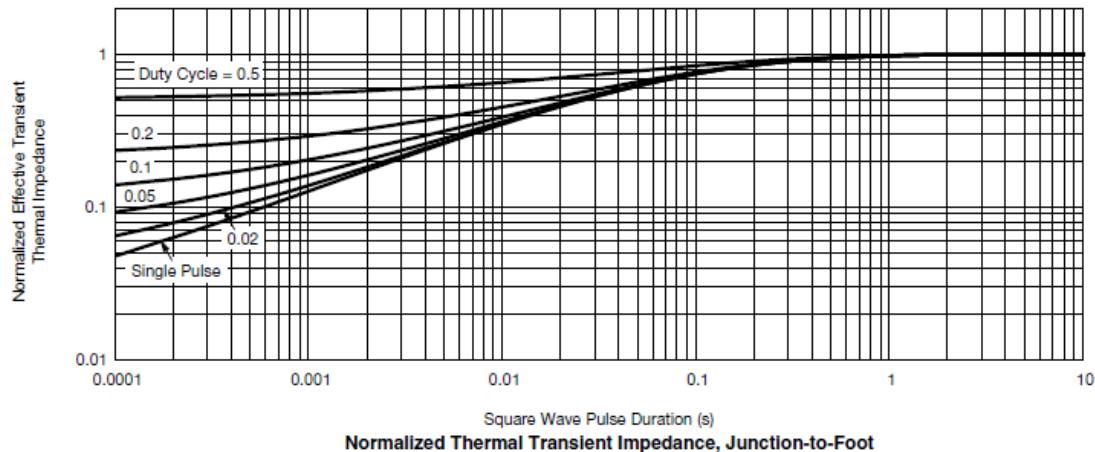
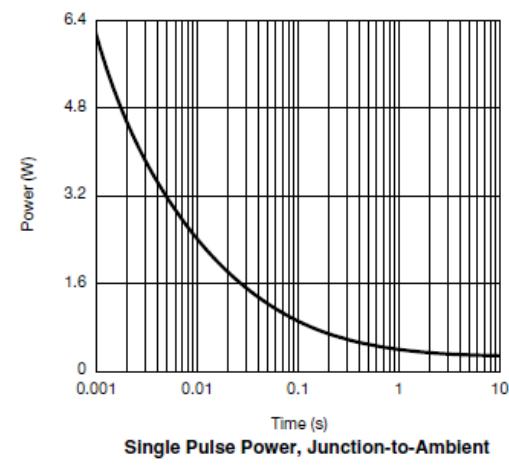
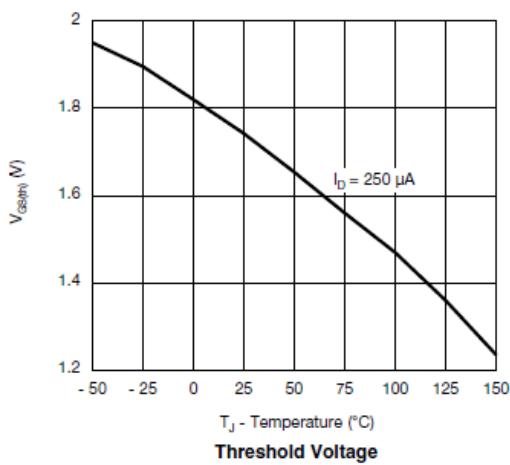
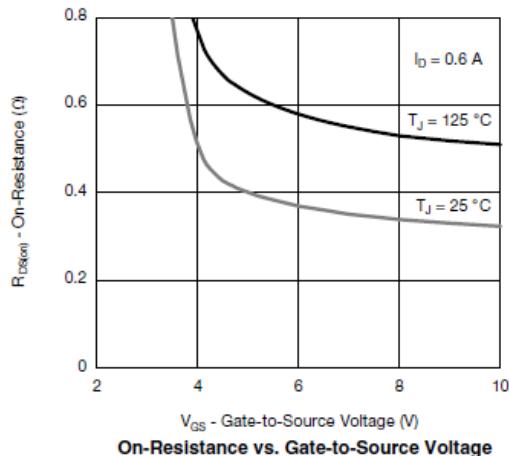
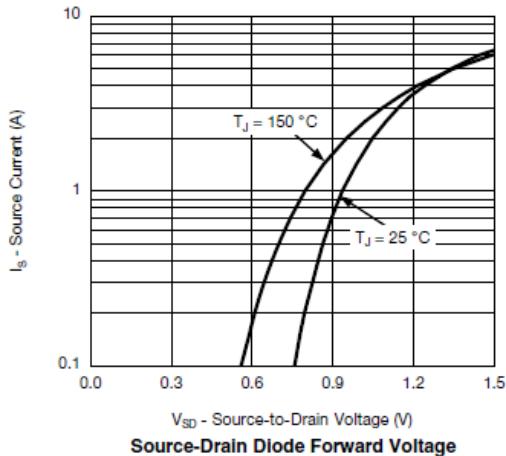
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### Typical Characteristics ( N-Channel )





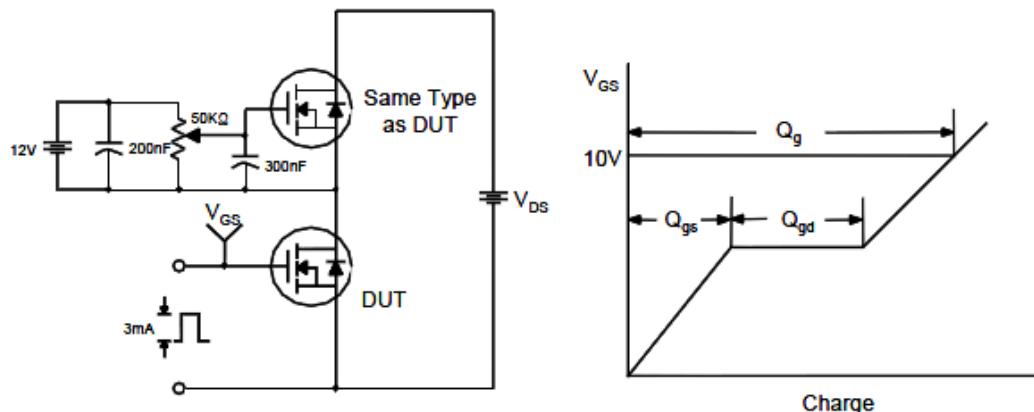
**Typical Characteristics ( N-Channel )**



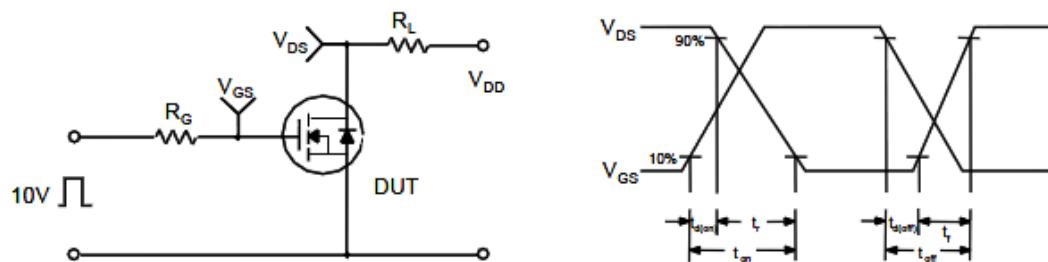


**Typical Characteristics ( N-Channel )**

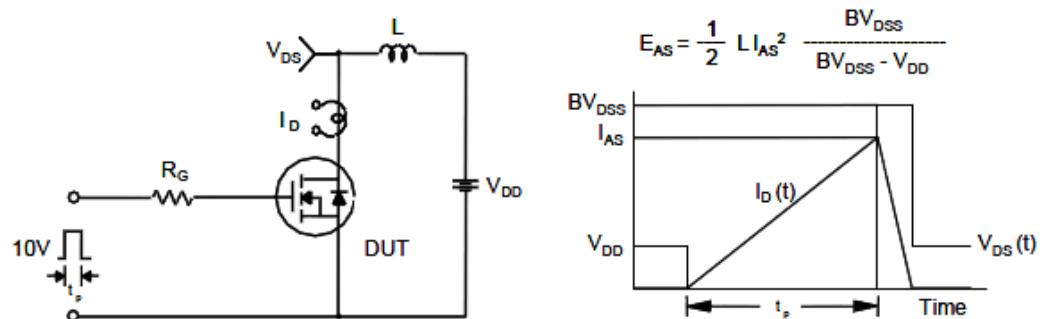
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

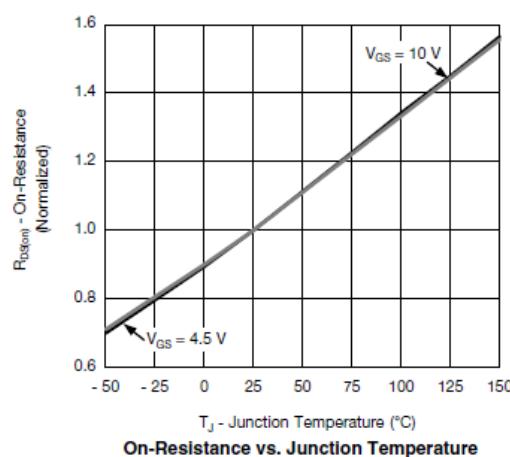
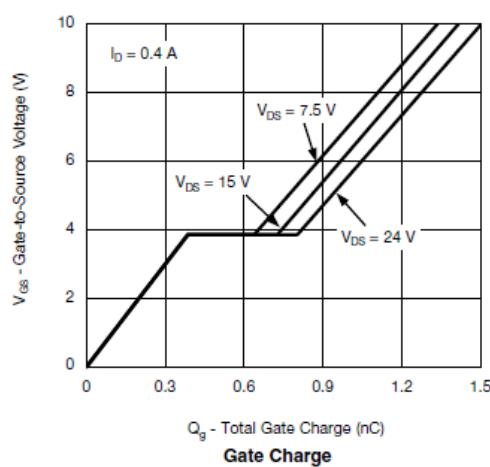
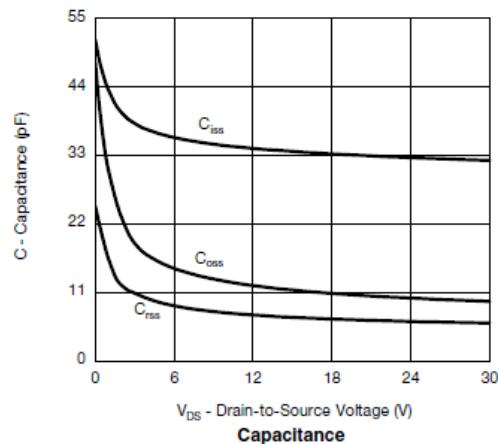
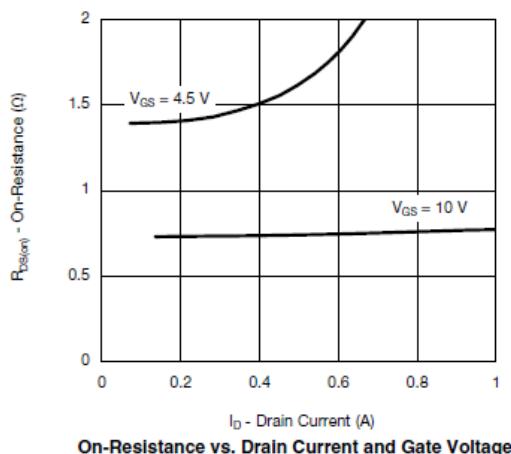
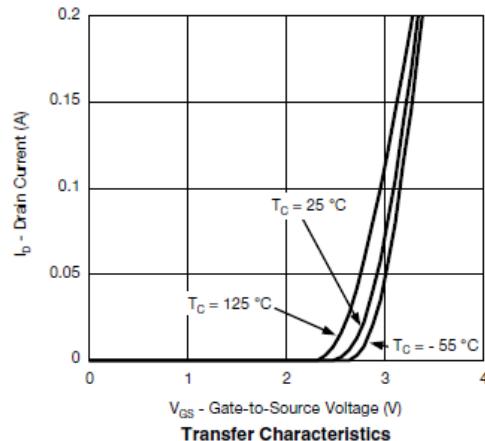
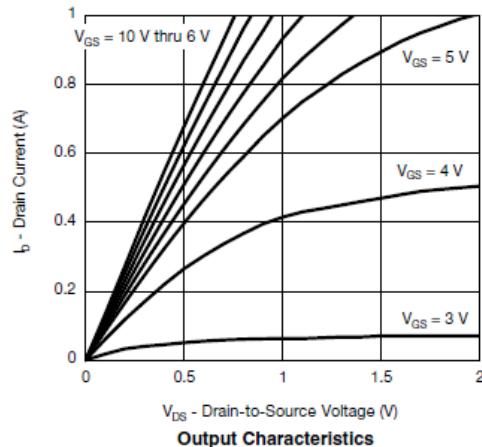




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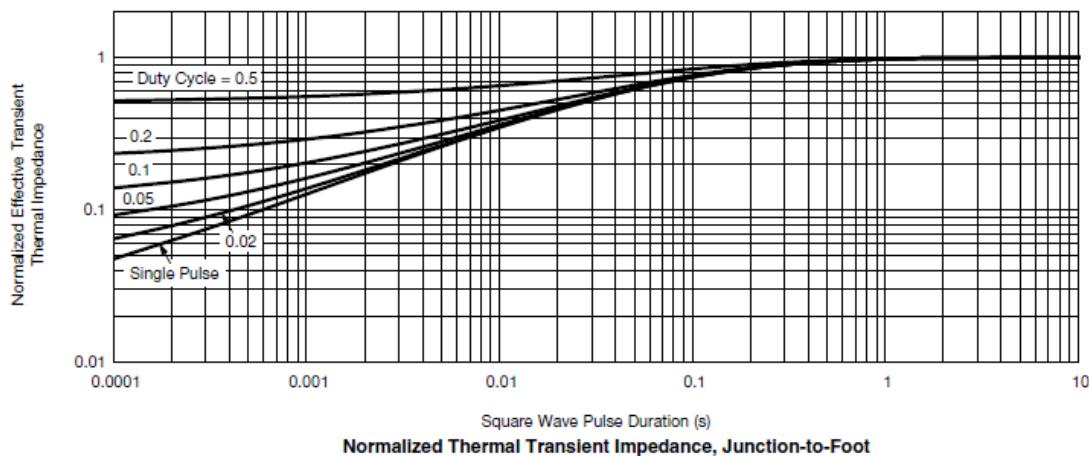
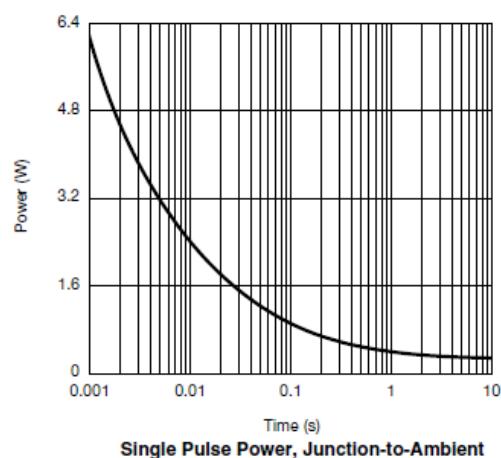
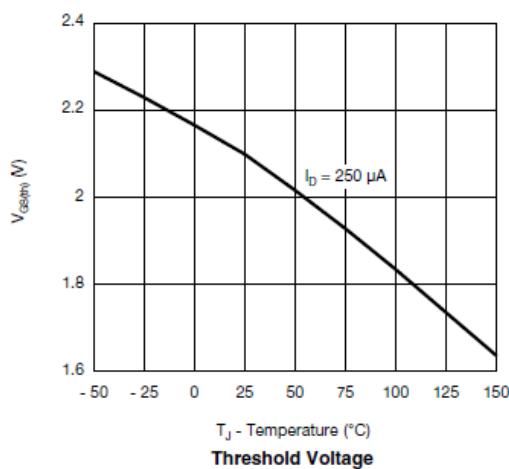
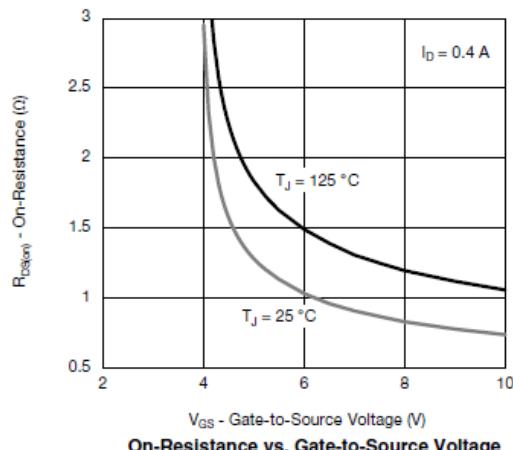
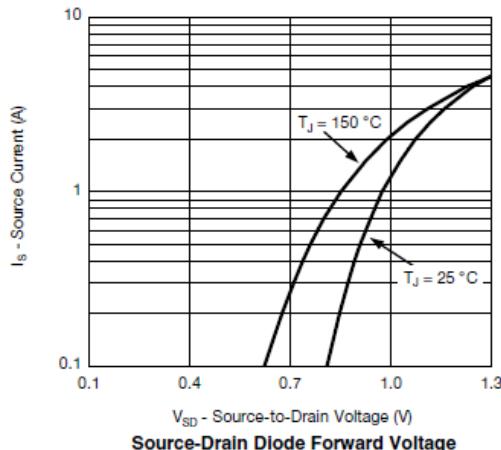




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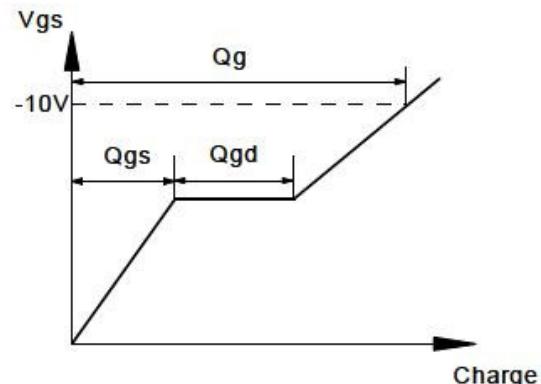
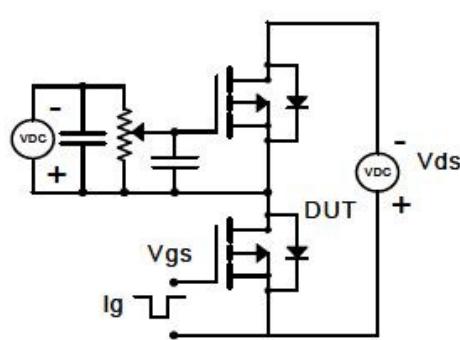
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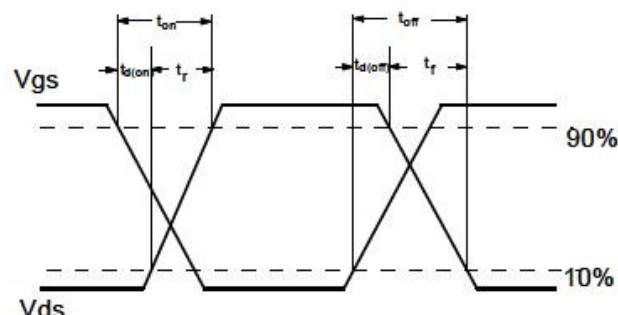
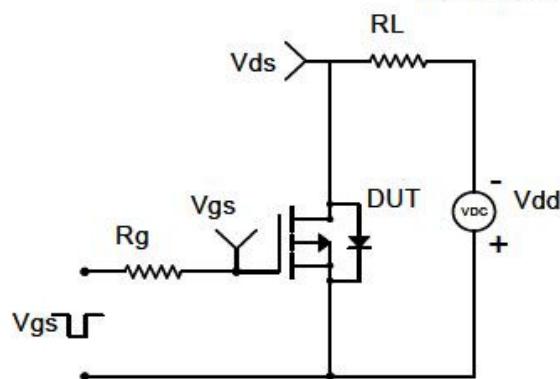


**Typical Characteristics ( P-Channel )**

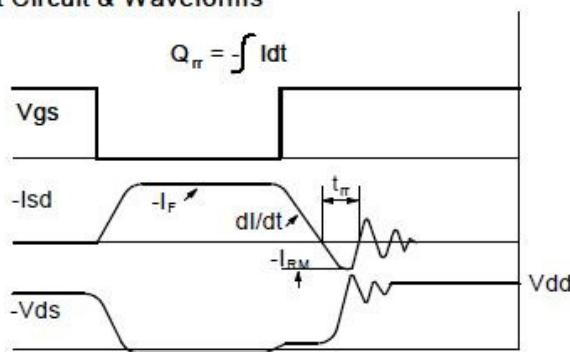
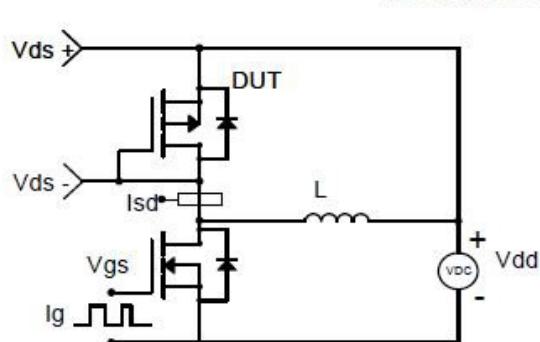
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

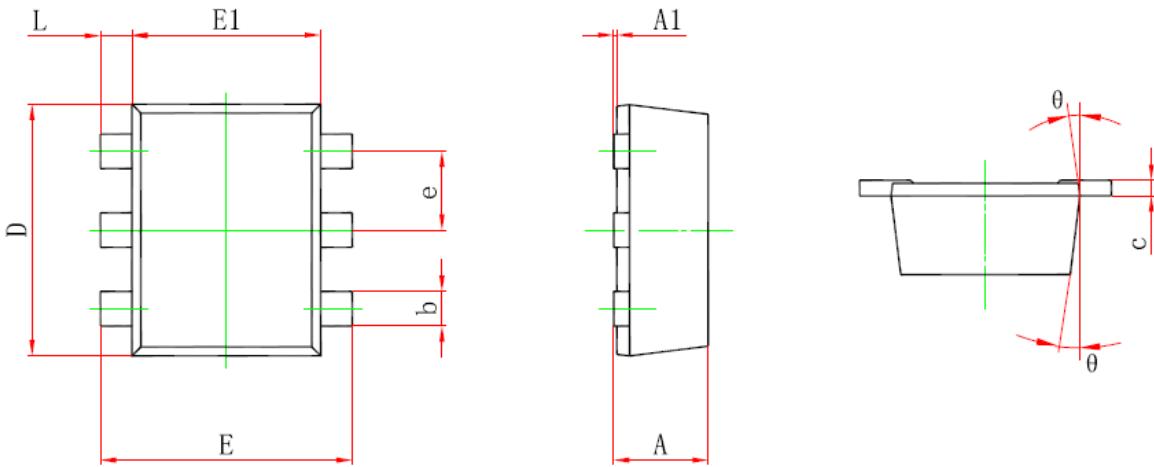


Diode Recovery Test Circuit & Waveforms





**Package Information ( SOT-563 )**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.525	0.600	0.021	0.024
A1	0.000	0.050	0.000	0.002
e	0.450	0.550	0.018	0.022
c	0.090	0.160	0.004	0.006
D	1.500	1.700	0.059	0.067
b	0.170	0.270	0.007	0.011
E1	1.100	1.300	0.043	0.051
E	1.500	1.700	0.059	0.067
L	0.100	0.300	0.004	0.012
θ	7°REF.		7°REF.	

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