



General Description

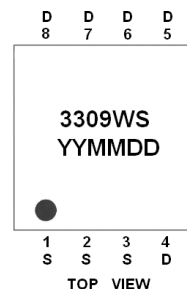
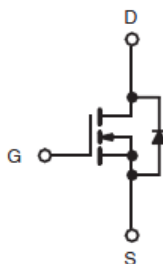
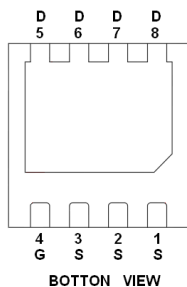
AFN3309WS, N-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent $R_{DS(ON)}$, low gate charge.

These devices are particularly suited for low voltage power management, such as smart phone and notebook computer and other battery powered circuits, and low in-line power loss are needed in commercial industrial surface mount applications.

Features

- $I_D=20A, R_{DS(ON)}=4.5m\Omega@V_{GS}=10V$
- $I_D=15A, R_{DS(ON)}=6.8m\Omega@V_{GS}=4.5V$
- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- DFN3X3-8L package design

Pin Description (DFN3X3-8L)



Application

- DC-DC Converter
- POL

Pin Define

Pin	Symbol	Description
1	S	Source
2	S	Source
3	S	Source
4	G	Gate
5	D	Drain
6	D	Drain
7	D	Drain
8	D	Drain

Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFN3309WSFN338RG	3309WS	DFN3X3-8L	Tape & Reel	5000 EA

※ YY year code

※ MM month code

※ DD date code

※ AFN3309WSFN338RG : 13" Tape & Reel ; Pb- Free ; Halogen -Free



Absolute Maximum Ratings

(T_A=25°C Unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	30	V
Gate –Source Voltage	V _{GSS}	±20	V
Continuous Drain Current(T _J =150°C)	I _D	T _A =25°C	35
		T _A =70°C	25
Pulsed Drain Current	I _{DM}	75	A
Continuous Source Current(Diode Conduction)	I _S	35	A
Power Dissipation	P _D	T _C =25°C	28
		T _C =70°C	15
		T _A =25°C	3.2
		T _A =70°C	2.0
Operating Junction Temperature	T _J	150	°C
Storage Temperature Range	T _{STG}	-55/150	°C
Thermal Resistance Junction-to-Case (Drain)	R _{θJC}	5	°C/W
Thermal Resistance-Junction to Ambient	R _{θJA}	40	

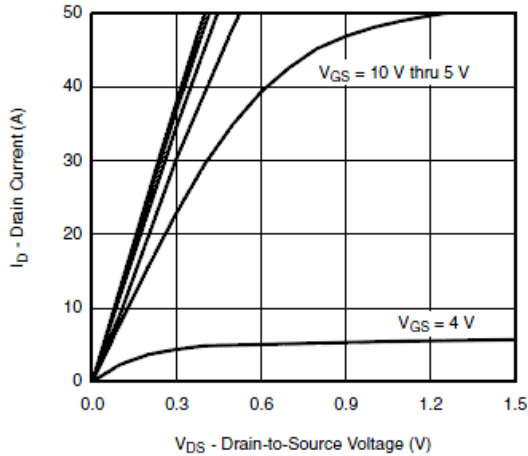
Electrical Characteristics

(T_A=25°C Unless otherwise noted)

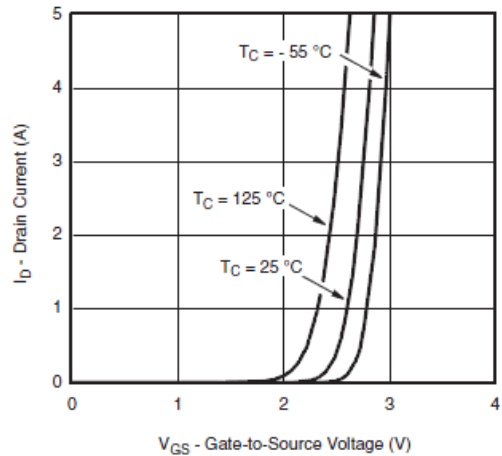
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250uA	30			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	1.0		2.0	
Gate Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =24V, V _{GS} =0V			1	uA
		V _{DS} =24V, V _{GS} =0V T _J =85°C			10	
On-State Drain Current	I _{D(on)}	V _{DS} ≥ 5V, V _{GS} =10V	15			A
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =10V, I _D =20A		3.2	4.5	mΩ
		V _{GS} =4.5V, I _D =15A		5.2	6.8	
Forward Transconductance	g _{FS}	V _{DS} =15V, I _D =10A		32		S
Diode Forward Voltage	V _{SD}	I _S =30A, V _{GS} =0V		0.8	1.3	V
Dynamic						
Total Gate Charge	Q _g	V _{DS} =15V, V _{GS} =4.5V I _D ≡15A		8	13	nC
Gate-Source Charge	Q _{gs}			3.7		
Gate-Drain Charge	Q _{gd}			2.7		
Input Capacitance	C _{iss}	V _{DS} =15V, V _{GS} =0V f=1MHz		1350		pF
Output Capacitance	C _{oss}			210		
Reverse Transfer Capacitance	C _{rss}			95		
Turn-On Time	t _{d(on)}	V _{DD} =15V, R _L =1.5Ω I _D ≡10A, V _{GEN} =10V R _G =1Ω		10	15	ns
	t _r			10	15	
Turn-Off Time	t _{d(off)}			25	35	
	t _f			10	15	



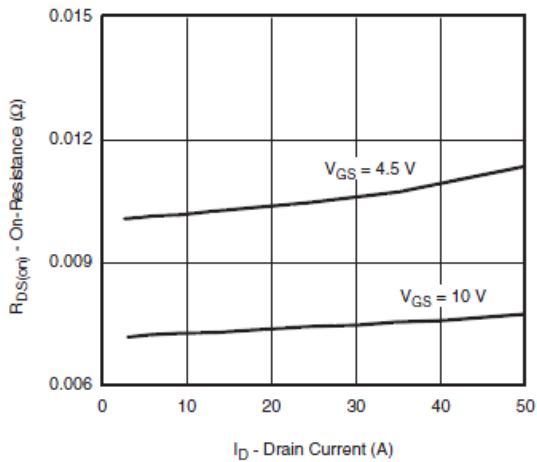
Typical Characteristics



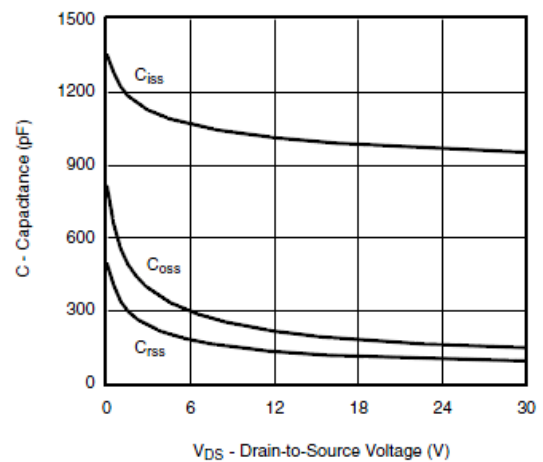
Output Characteristics



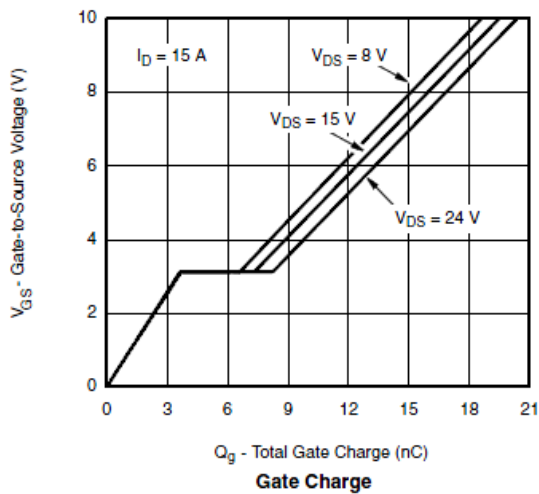
Transfer Characteristics



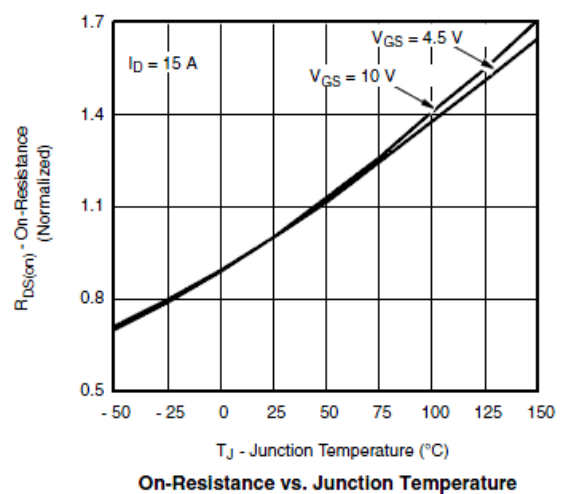
On-Resistance vs. Drain Current and Gate Voltage



Capacitance



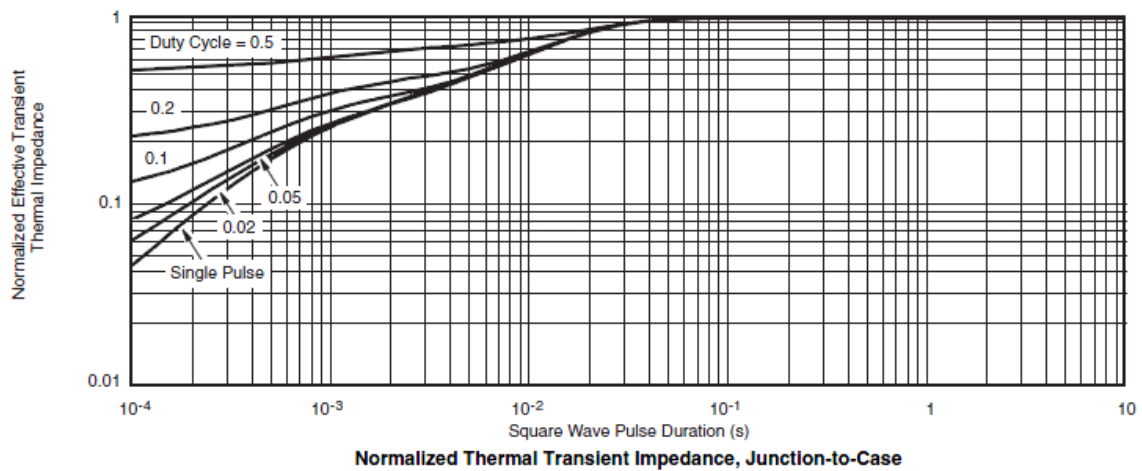
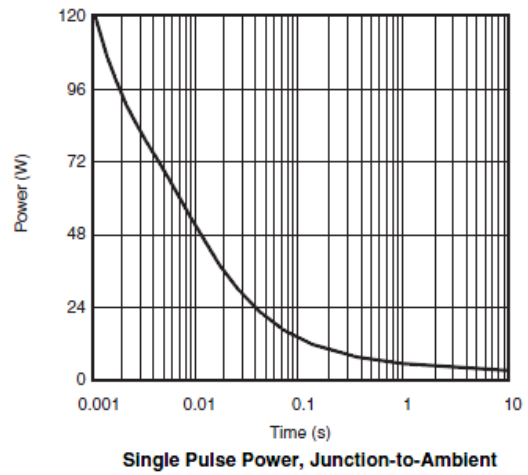
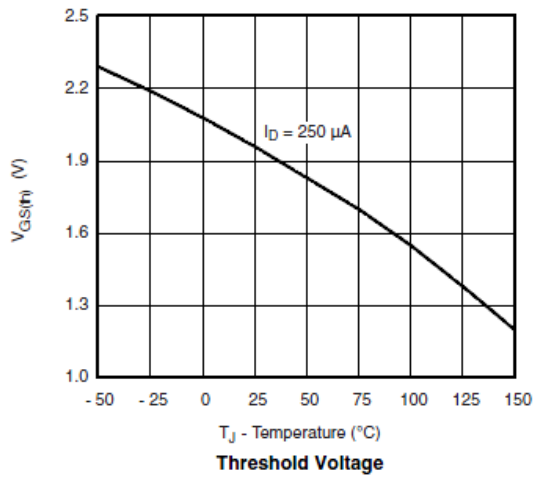
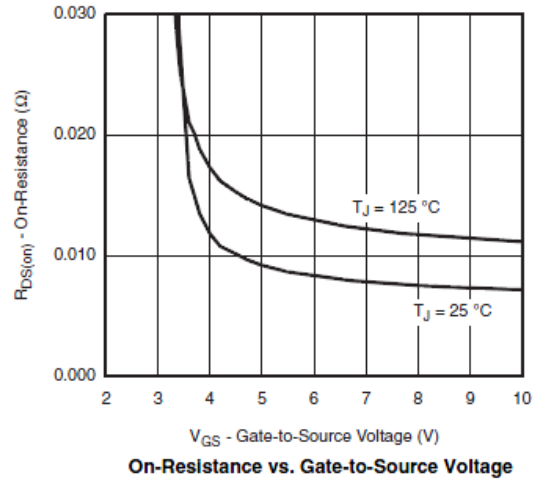
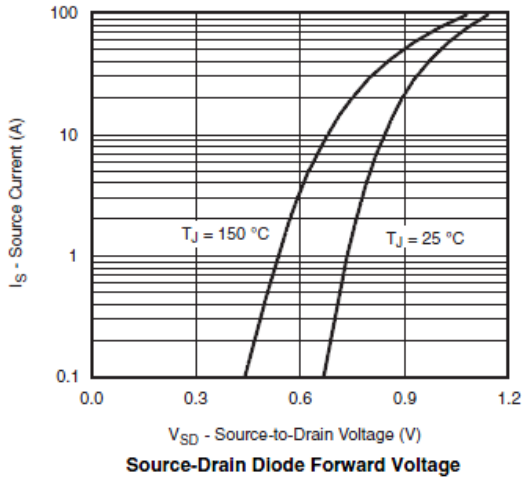
Gate Charge



On-Resistance vs. Junction Temperature



Typical Characteristics





Typical Characteristics

Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

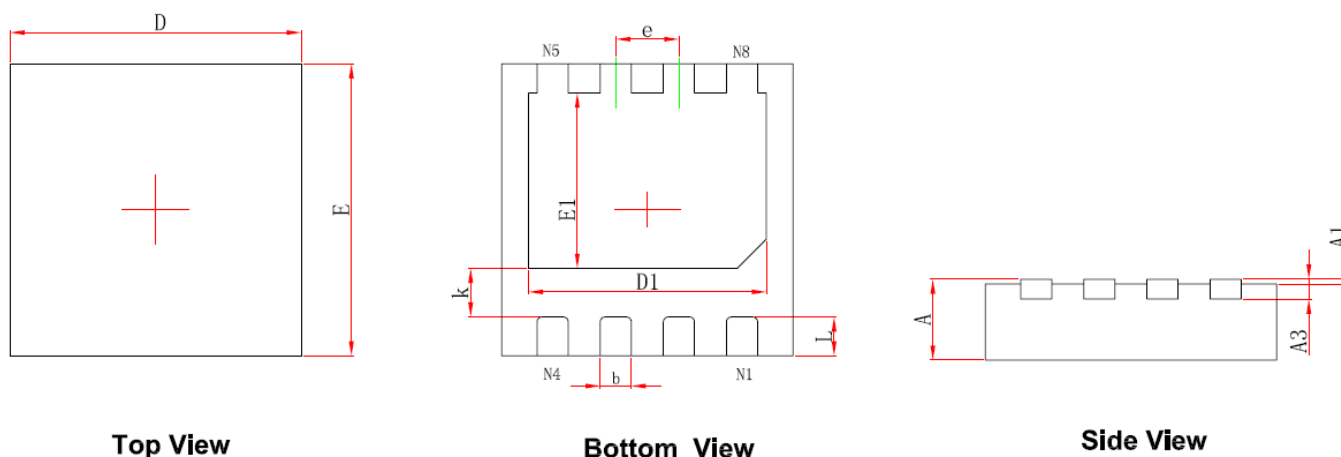


Unclamped Inductive Switching Test Circuit & Waveforms





Package Information (DFN3X3-8L)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.800	0.900	0.031	0.035
A1	0.000	0.050	0.000	0.002
A3	0.203REF.		0.008REF.	
D	2.924	3.076	0.115	0.121
E	2.924	3.076	0.115	0.121
D1	2.350	2.550	0.093	0.100
E1	1.700	1.900	0.067	0.075
k	0.450	0.550	0.018	0.022
b	0.270	0.370	0.011	0.015
e	0.650TYP.		0.026TYP.	
L	0.324	0.476	0.013	0.019

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