



### General Description

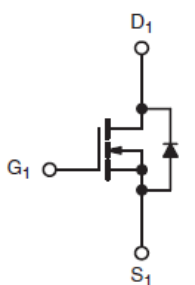
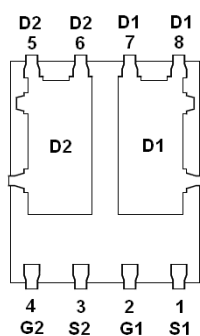
AFN5853AS, Dual N-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent  $R_{DS(ON)}$ , low gate charge.

These devices are particularly suited for low voltage power management, and low in-line power loss are needed in commercial industrial surface mount applications.

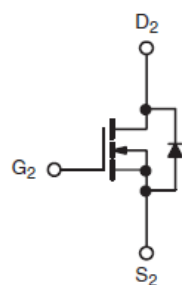
### Features

- $I_D=18A, R_{DS(ON)}= 9m\Omega@V_{GS}=10V$
- $I_D=15A, R_{DS(ON)}=11m\Omega@V_{GS}=4.5V$
- Super high density cell design for extremely low  $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- DFN 5X6-8L package design

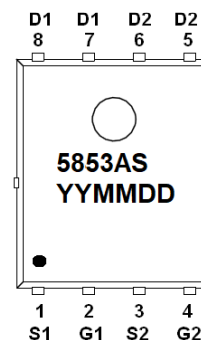
### Pin Description ( DFN5X6-8L )



N-Channel MOSFET



N-Channel MOSFET



### Application

- POL
- DC/DC

### Pin Define

Pin	Symbol	Description
1	S1	Source 1
2	G1	Gate 1
3	S2	Source 2
4	G2	Gate 2
5	D2	Drain 2
6	D2	Drain 2
7	D1	Drain 1
8	D1	Drain 1

### Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFN5853ASFN568RG	5853AS	DFN 5X6-8L	Tape & Reel	2500 EA

※ 5853AS : Parts Code

※ YYMMDD : Date Code

※ AFN5853ASFN568RG : 13" Tape & Reel ; Pb- Free ; Halogen -Free



**Absolute Maximum Ratings** ( $T_A=25^{\circ}\text{C}$  Unless otherwise noted)

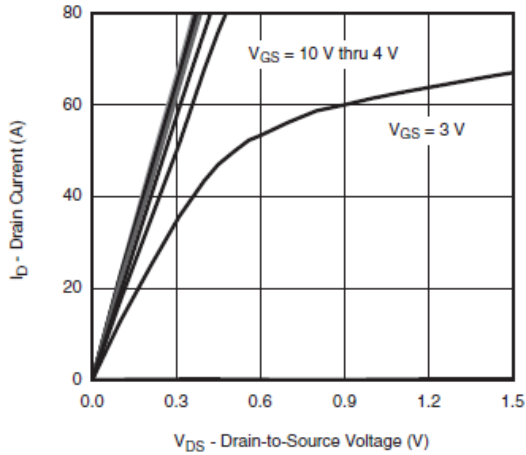
Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	40	V
Gate –Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current( $T_J=150^{\circ}\text{C}$ )	$I_D$	$T_C=25^{\circ}\text{C}$	60
		$T_C=85^{\circ}\text{C}$	60
		$T_A=25^{\circ}\text{C}$	20
		$T_A=85^{\circ}\text{C}$	16
Pulsed Drain Current	$I_{DM}$	80	A
Continuous Source Current(Diode Conduction)	$I_S$	$T_C=25^{\circ}\text{C}$	38
		$T_A=25^{\circ}\text{C}$	2.9
Power Dissipation	$P_D$	$T_C=25^{\circ}\text{C}$	46
		$T_C=85^{\circ}\text{C}$	29
		$T_A=25^{\circ}\text{C}$	3.5
		$T_A=70^{\circ}\text{C}$	2.2
Operating Junction Temperature	$T_J$	150	$^{\circ}\text{C}$
Storage Temperature Range	$T_{STG}$	-55/150	$^{\circ}\text{C}$
Thermal Resistance-Junction to Ambient	$R_{\theta JA}$	62.5	$^{\circ}\text{C}/\text{W}$

**Electrical Characteristics** ( $T_A=25^{\circ}\text{C}$  Unless otherwise noted)

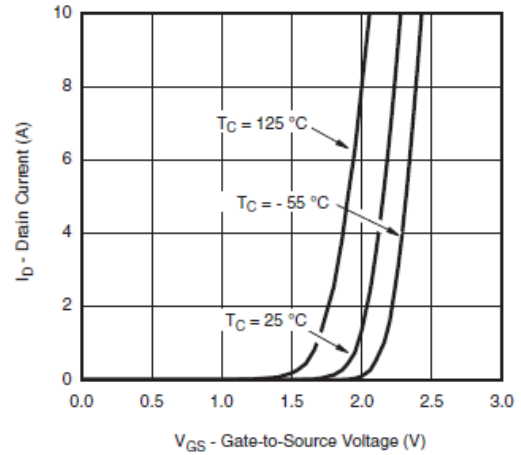
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	40			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	1.8	2.5	
Gate Leakage Current	$I_{GSS}$	$V_{DS}=0V, V_{GS}=\pm 20V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=32V, V_{GS}=0V$			1	uA
		$V_{DS}=32V, V_{GS}=0V$ $T_J=85^{\circ}\text{C}$			10	
On-State Drain Current	$I_{D(on)}$	$V_{DS} \geq 5V, V_{GS}=10V$	20			A
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=18A$		6.5	9	m $\Omega$
		$V_{GS}=4.5V, I_D=15A$		8	11	
Forward Transconductance	$g_{FS}$	$V_{DS}=15V, I_D=20A$		70		S
Diode Forward Voltage	$V_{SD}$	$I_S=5A, V_{GS}=0V$		0.8	1.3	V
<b>Dynamic</b>						
Total Gate Charge	$Q_g$	$V_{DS}=20V, V_{GS}=4.5V$ $I_D \equiv 18.5A$		21	32	nC
Gate-Source Charge	$Q_{gs}$			6.2		
Gate-Drain Charge	$Q_{gd}$			6.2		
Input Capacitance	$C_{iss}$	$V_{DS}=20V, V_{GS}=0V$ $f=1\text{MHz}$		2300		pF
Output Capacitance	$C_{oss}$			340		
Reverse Transfer Capacitance	$C_{rss}$			140		
Turn-On Time	$t_{d(on)}$	$V_{DD}=20V, R_L=2.0\Omega$ $I_D \equiv 10A, V_{GEN}=10V$ $R_G=1.0\Omega$		10	20	ns
	$t_r$			10	20	
Turn-Off Time	$t_{d(off)}$			30	55	
	$t_f$			10	20	



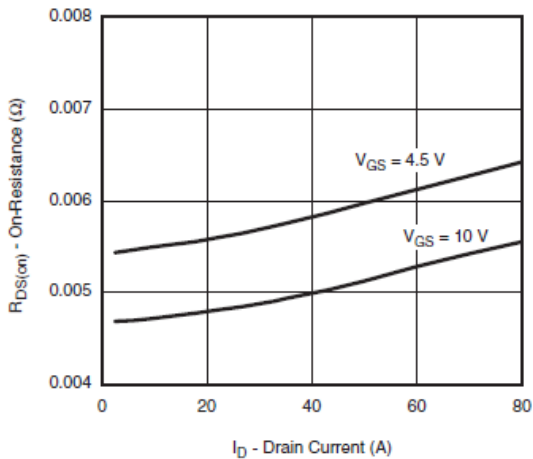
## Typical Characteristics



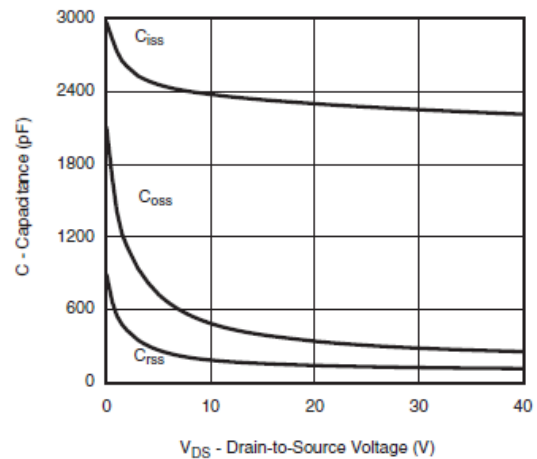
Output Characteristics



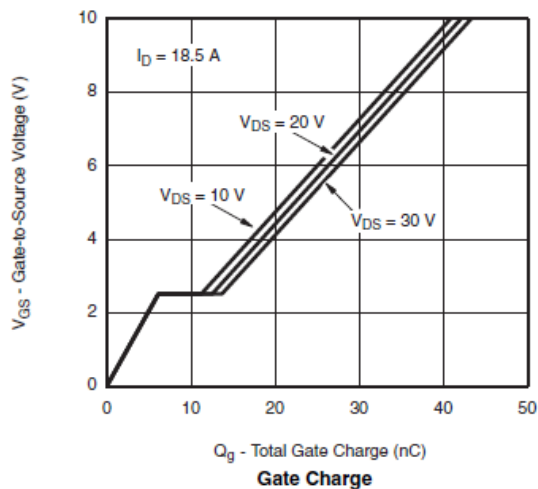
Transfer Characteristics



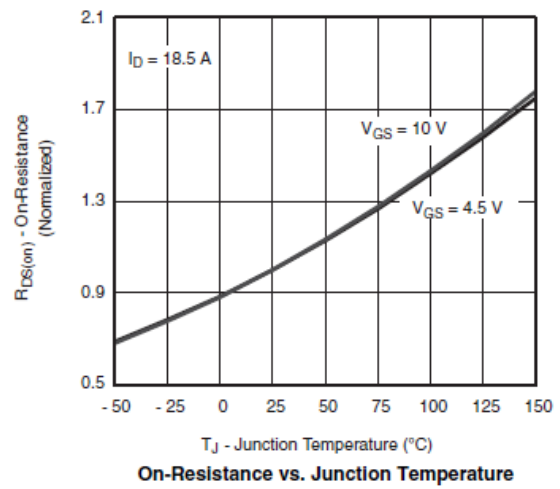
On-Resistance vs. Drain Current and Gate Voltage



Capacitance



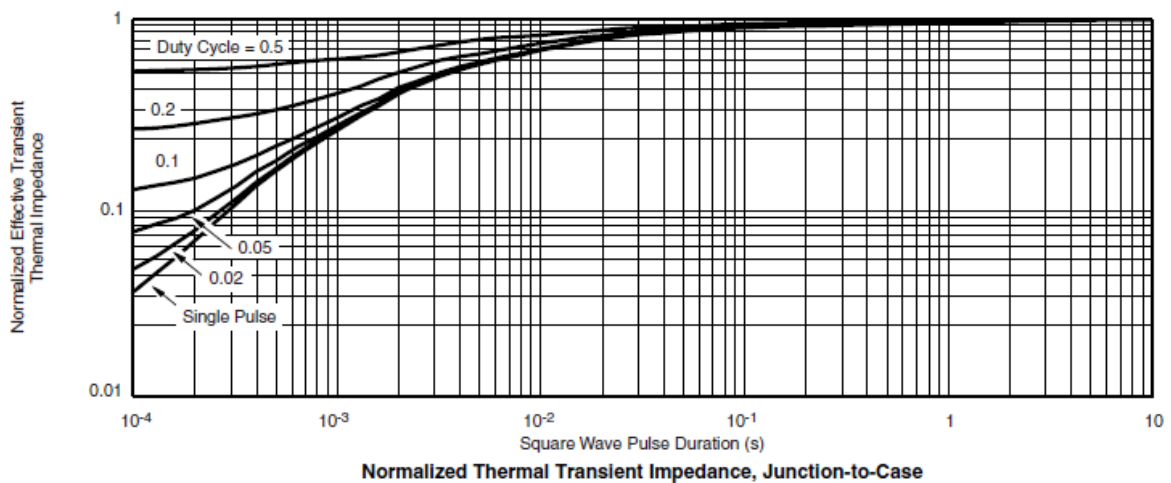
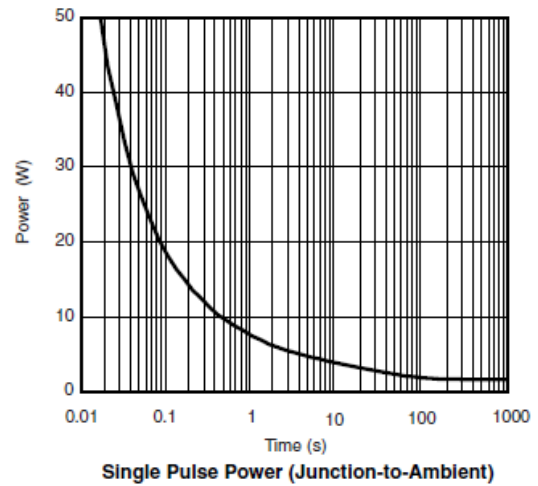
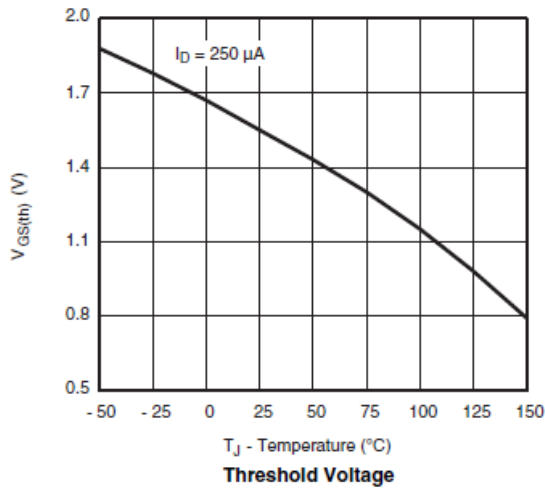
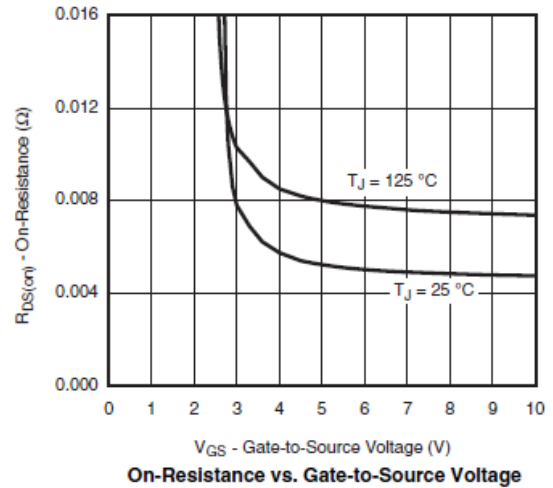
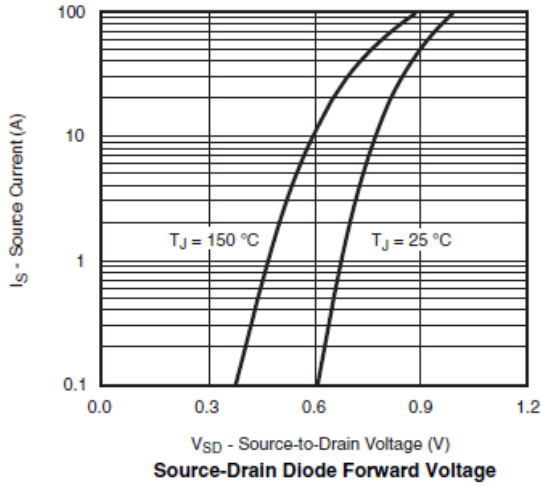
Gate Charge



On-Resistance vs. Junction Temperature



### Typical Characteristics





**Typical Characteristics**

Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

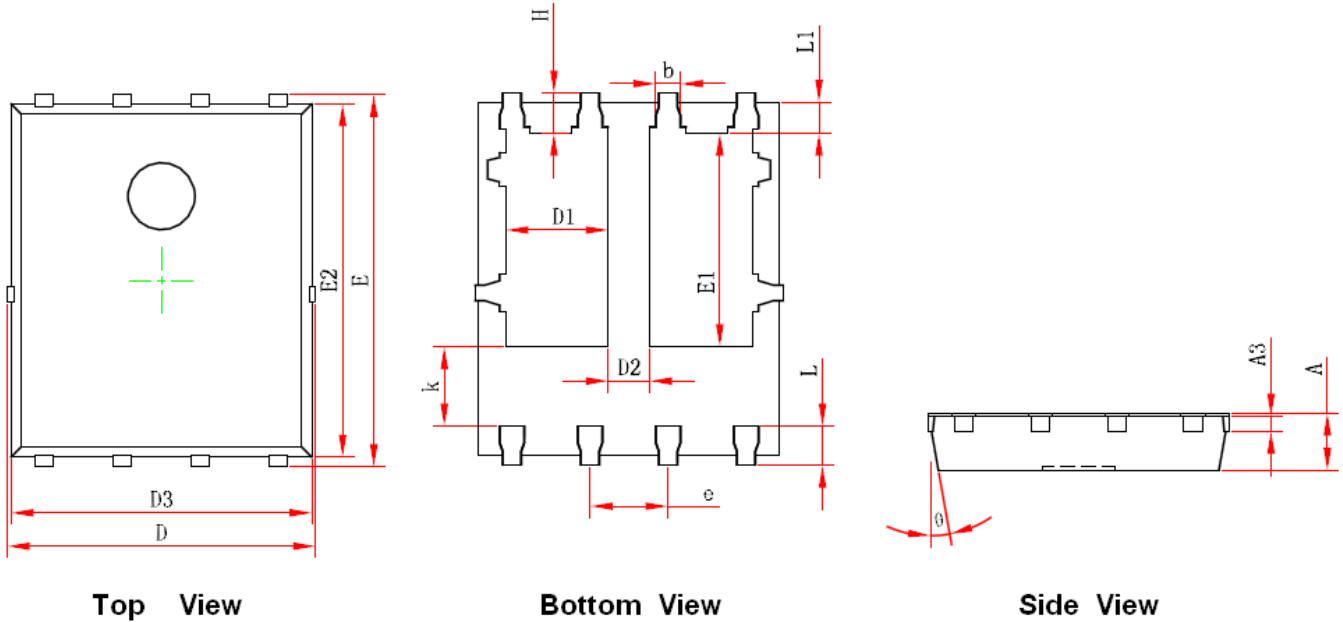


Unclamped Inductive Switching Test Circuit & Waveforms





**Package Information ( DFN 5X6-8L )**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039
A3	0.254 REF.		0.010REF.	
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	1.470	1.870	0.058	0.074
D2	0.470	0.870	0.019	0.034
E1	3.375	3.575	0.133	0.141
D3	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
$\theta$	10°	12°	10°	12°

©2010 Alfa-MOS Technology Corp.  
 2F, No.80, Sec.1, Cheng Kung Rd., Nan Kang Dist., Taipei City 115, Taiwan (R.O.C.)  
 Tel : 886 2) 2651 3928  
 Fax : 886 2) 2786 8483  
 ©http://www.alfa-mos.com