



## General Description

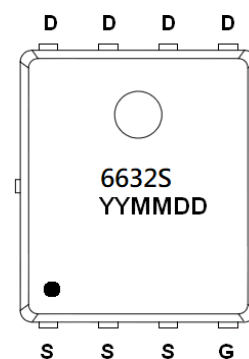
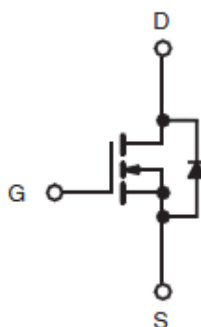
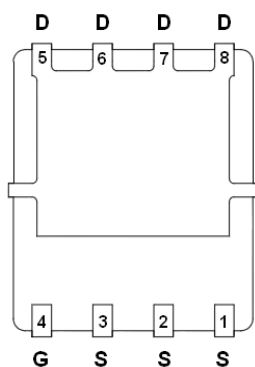
AFN6632S, N-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent  $R_{DS(ON)}$ , low gate charge.

These devices are particularly suited for low voltage power management, and low in-line power loss are needed in commercial industrial surface mount applications.

## Features

- $I_D=8A, R_{DS(ON)}=45m\Omega @ V_{GS}=10V$
- $I_D=6A, R_{DS(ON)}=58m\Omega @ V_{GS}=4.5V$
- Super high density cell design for extremely low  $R_{DS(ON)}$
- DFN5X6-8L package design

## Pin Description ( DFN5X6-8L )



## Application

- Synchronous Rectifier
- Power Supplies
- LED TV

## Pin Define

Pin	Symbol	Description
1~3	S	Source
4	G	Gate
5~8	D	Drain

## Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFN6632SFN568RG	6632S	DFN5X6-8L	Tape & Reel	2500 EA

- ※ 6632S : Parts Code
- ※ YYMMDD : Date Code
- ※ AFN6632SFN568RG : 13" Tape & Reel ; Pb- Free ; Halogen- Free



### Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ Unless otherwise noted)

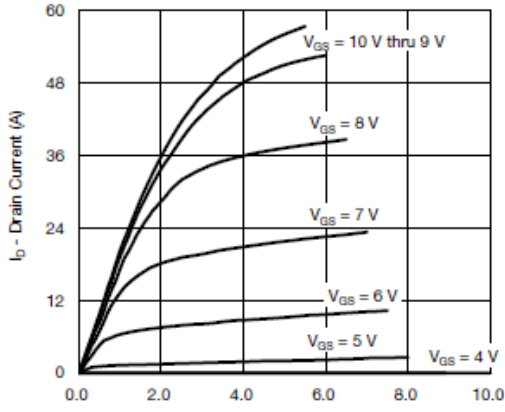
Parameter	Symbol	Typical	Unit
Drain-Source Voltage	$V_{DSS}$	150	V
Gate –Source Voltage	$V_{GSS}$	$\pm 20$	V
Continuous Drain Current ( $T_J=150^\circ\text{C}$ )	$I_{DSM}$	$T_C=25^\circ\text{C}$	20
		$T_C=70^\circ\text{C}$	16
		$T_A=25^\circ\text{C}$	8
		$T_A=70^\circ\text{C}$	6
Pulsed Drain Current	$I_{DM}$	50	
Continuous Source Current (Diode Conduction)	$I_S$	35	
Single Pulse Avalanche Current	$L = 0.1 \text{ mH}$	$I_{AS}$	8
		$E_{AS}$	16
Power Dissipation	$P_{DSM}$	$T_A=25^\circ\text{C}$	3.7
		$T_A=75^\circ\text{C}$	2.4
Operating Junction Temperature	$T_J$	150	$^\circ\text{C}$
Storage Temperature Range	$T_{STG}$	-55/150	$^\circ\text{C}$
Thermal Resistance-Junction to Ambient	Steady-State $R_{\theta JA}$	40	$^\circ\text{C/W}$

### Electrical Characteristics ( $T_A=25^\circ\text{C}$ Unless otherwise noted)

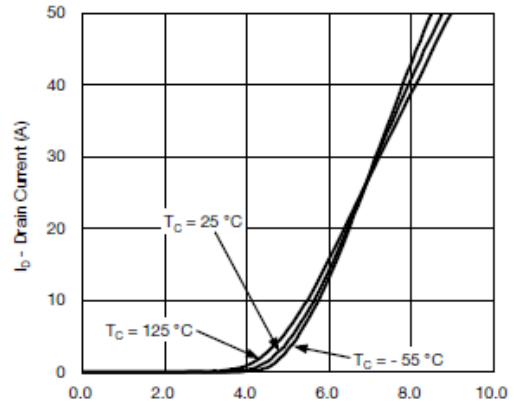
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu\text{A}$	150			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.0	1.85	2.5	
Gate Leakage Current	$I_{GSS}$	$V_{DS}=0V, V_{GS}=\pm 20V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=120V, V_{GS}=0V$			1	uA
		$V_{DS}=120V, V_{GS}=0V$ $T_J=85^\circ\text{C}$			10	
On-State Drain Current	$I_{D(on)}$	$V_{DS} \geq 10V, V_{GS}=10V$	40			A
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=8A$		33	45	m $\Omega$
		$V_{GS}=4.5V, I_D=6A$		37	58	
Forward Transconductance	$g_{FS}$	$V_{DS}=15V, I_D=8A$		12		S
Diode Forward Voltage	$V_{SD}$	$I_S=4.0A, V_{GS}=0V$		0.8	1.2	V
<b>Dynamic</b>						
Total Gate Charge	$Q_g$	$V_{DS}=75V, V_{GS}=7.5V$ $I_D=8A$		8	16	nC
Gate-Source Charge	$Q_{gs}$			2.5		
Gate-Drain Charge	$Q_{gd}$			3.5		
Gate resistance	$R_g$		0.2	1.3	2.0	$\Omega$
Input Capacitance	$C_{iss}$	$V_{DS}=75V, V_{GS}=0V$ $f=1\text{MHz}$		450		pF
Output Capacitance	$C_{oss}$			135		
Reverse Transfer Capacitance	$C_{rss}$			15		
Turn-On Time	$t_{d(on)}$	$V_{DD}=75V, R_L=5\Omega$ $I_D=8A, V_{GEN}=10V$ $R_G=1.0\Omega$		15	30	ns
	$t_r$			10	20	
Turn-Off Time	$t_{d(off)}$			15	30	
	$t_f$			10	20	



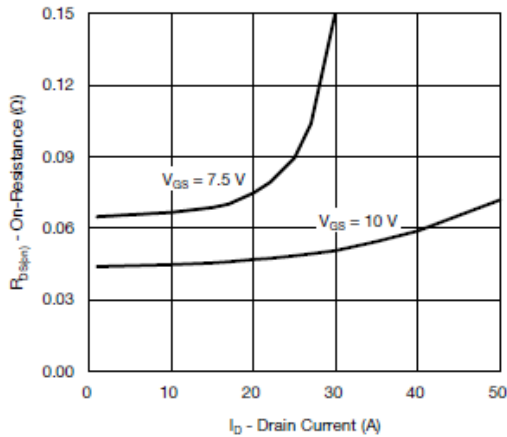
## Typical Characteristics



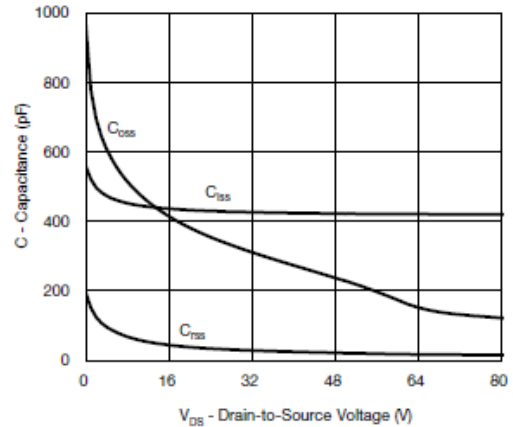
Output Characteristics



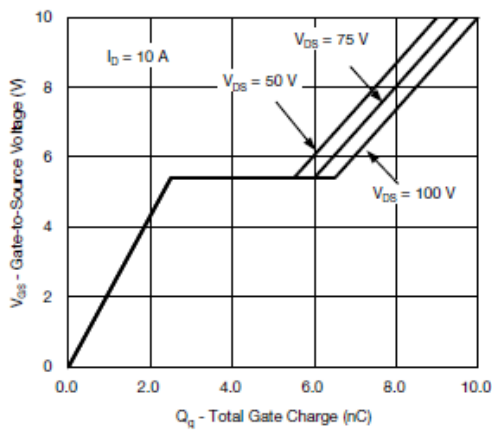
Transfer Characteristics



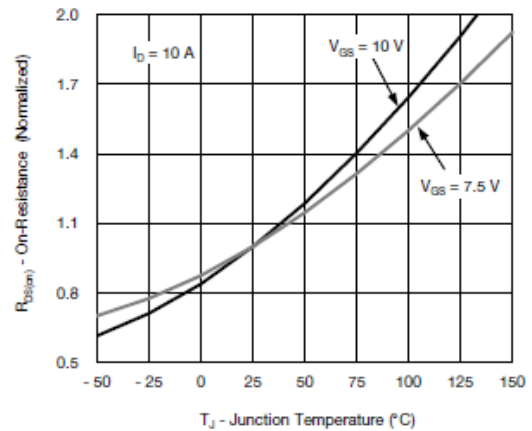
On-Resistance vs. Drain Current and Gate Voltage



Capacitance



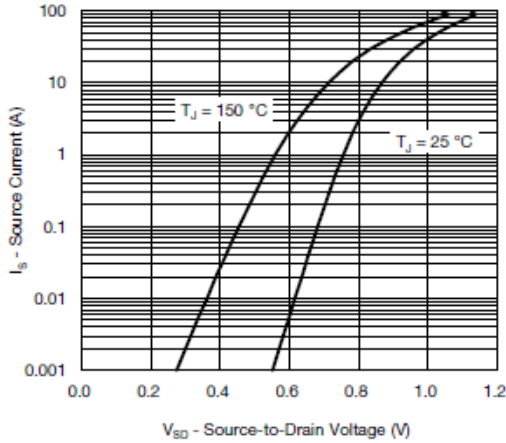
Gate Charge



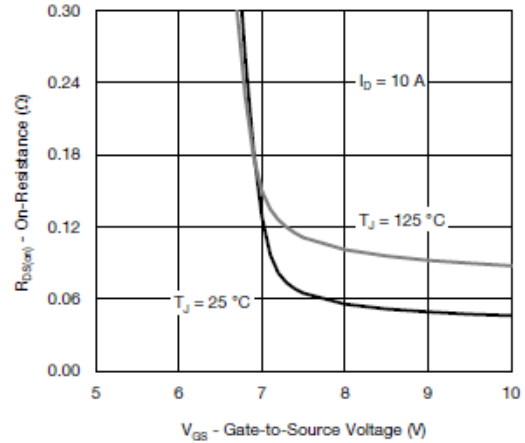
On-Resistance vs. Junction Temperature



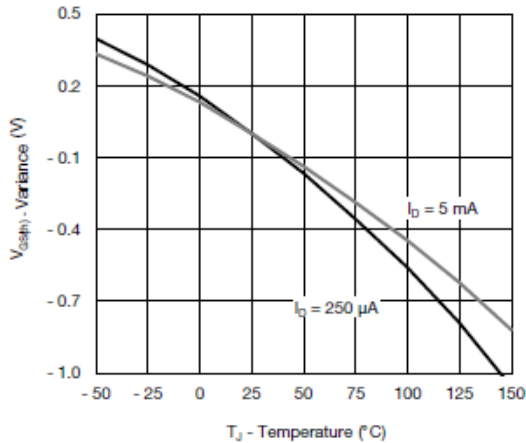
## Typical Characteristics



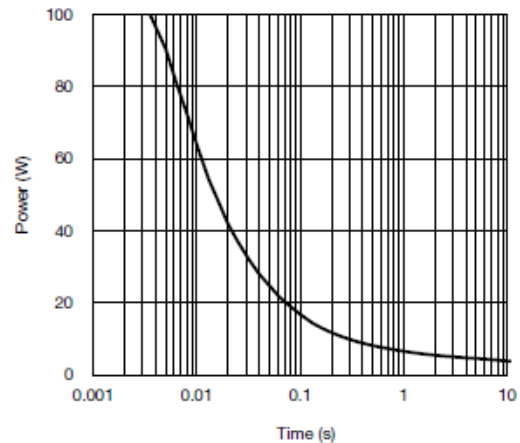
Source-Drain Diode Forward Voltage



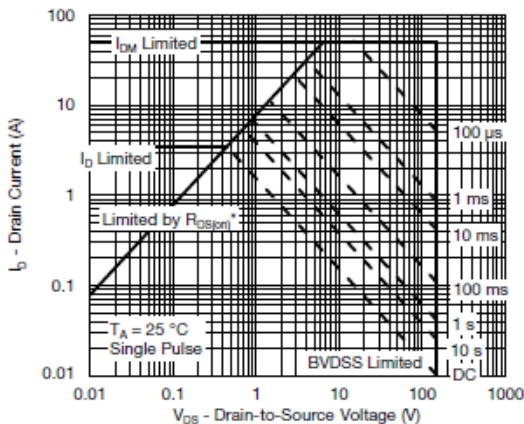
On-Resistance vs. Gate-to-Source Voltage



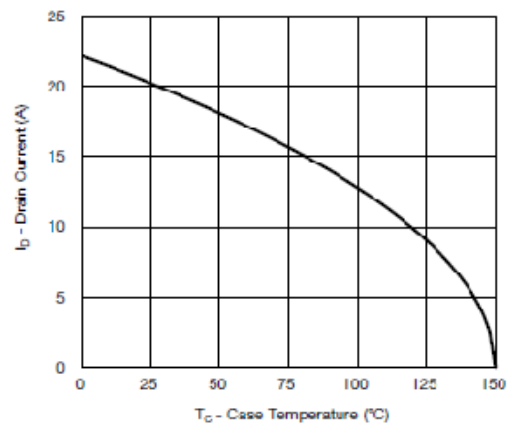
Threshold Voltage



Single Pulse Power, Junction-to-Ambient



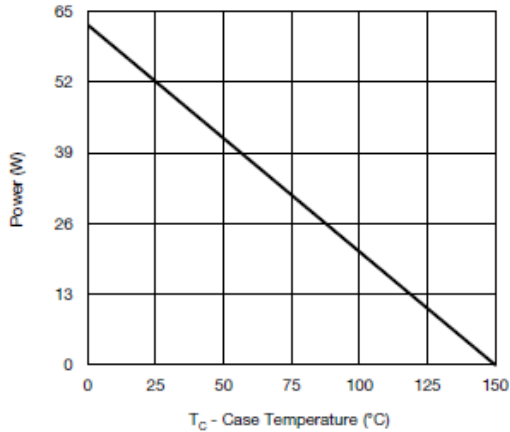
Safe Operating Area, Junction-to-Ambient



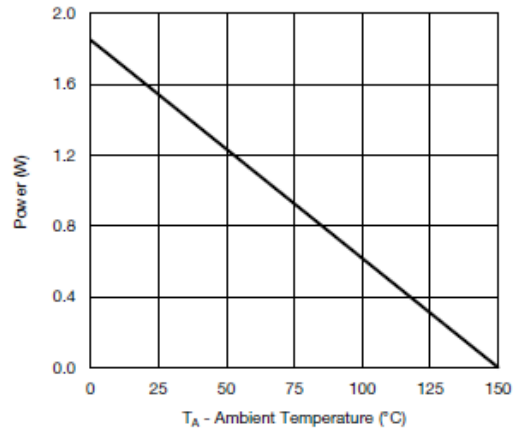
Current Derating\*



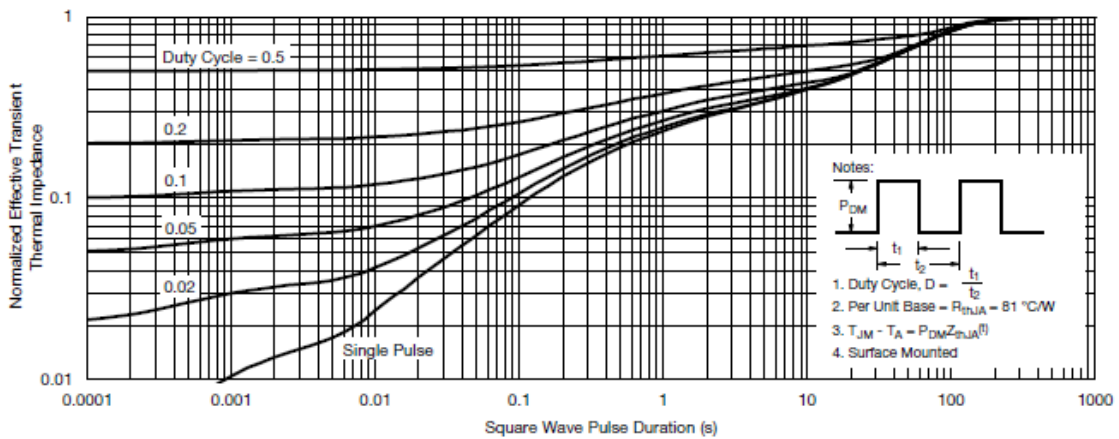
## Typical Characteristics



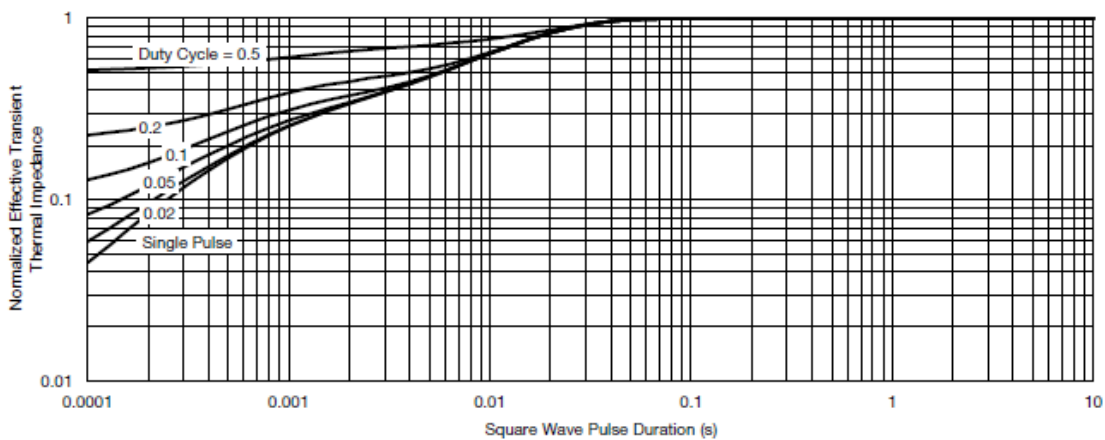
Power, Junction-to-Case



Power, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case



**Typical Characteristics**

Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

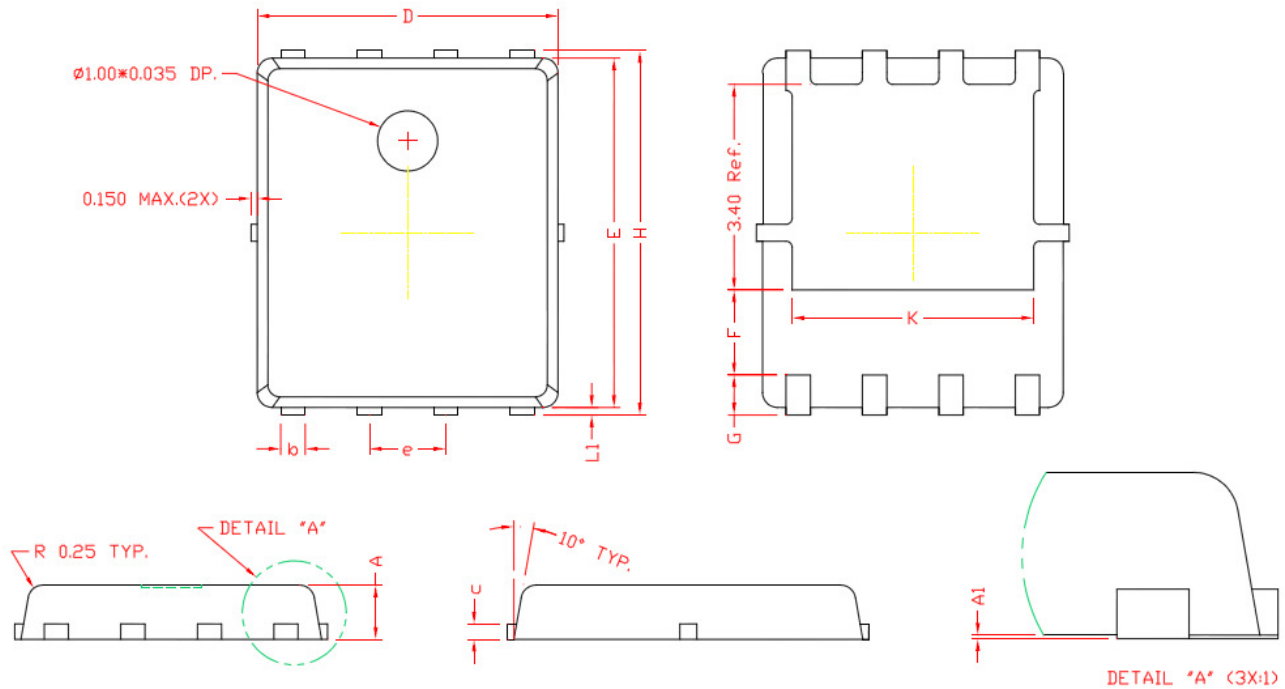


Unclamped Inductive Switching Test Circuit & Waveforms





**Package Information ( DFN5X6-8L )**



**DIMENSIONS**

REF.	Millimeters		REF.	Millimeters	
	Min.	Max.		Min.	Max.
A	0.80	1.00	E	5.70	5.90
A1	0.00	0.05	e	1.27 BSC.	
b	0.35	0.49	H	5.95	6.20
c	0.254 Ref.		L1	0.10	0.18
D	4.90	5.10	G	0.60 Ref.	
F	1.40 Ref.		K	4.00 Ref.	

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