



## General Description

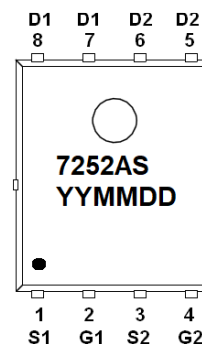
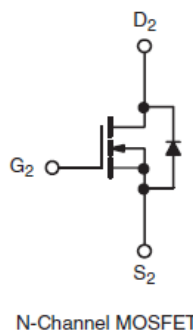
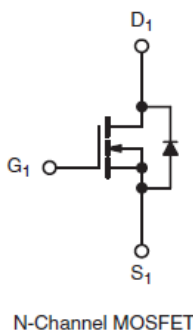
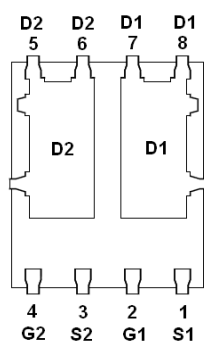
AFN7252AS, Dual N-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent  $R_{DS(ON)}$ , low gate charge.

These devices are particularly suited for low voltage power management, and low in-line power loss are needed in commercial industrial surface mount applications.

## Features

- $I_D=12A, R_{DS(ON)}= 22m\Omega@V_{GS}=10V$
- $I_D=10A, R_{DS(ON)}= 30m\Omega@V_{GS}=4.5V$
- Super high density cell design for extremely low  $R_{DS(ON)}$
- DFN 5X6-8L package design

## Pin Description ( DFN5X6-8L )



## Application

- Motor and Load Control
- Power Management in White LED System
- LCD TV Inverter & AD/DC Inverter Systems.

## Pin Define

Pin	Symbol	Description
1	S1	Source 1
2	G1	Gate 1
3	S2	Source 2
4	G2	Gate 2
5	D2	Drain 2
6	D2	Drain 2
7	D1	Drain 1
8	D1	Drain 1

## Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFN7252ASFN568RG	7252AS	DFN 5X6-8L	Tape & Reel	2500 EA

※ 7252AS : Parts Code

※ YYMMDD : Date Code

※ AFN7252ASFN568RG : 13" Tape & Reel ; Pb- Free ; Halogen -Free



### Absolute Maximum Ratings (T<sub>A</sub>=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	100	V	
Gate –Source Voltage	V <sub>GSS</sub>	±20	V	
Continuous Drain Current(T <sub>J</sub> =150°C)	T <sub>A</sub> =25°C	12	A	
	T <sub>A</sub> =85°C	10		
Pulsed Drain Current	I <sub>DM</sub>	60	A	
Continuous Source Current(Diode Conduction)	T <sub>A</sub> =25°C	I <sub>S</sub>	3.3	A
Power Dissipation	T <sub>A</sub> =25°C	P <sub>D</sub>	3.6	W
	T <sub>A</sub> =70°C		2.3	
Operating Junction Temperature	T <sub>J</sub>	150	°C	
Storage Temperature Range	T <sub>STG</sub>	-55/150	°C	
Thermal Resistance-Junction to Ambient	R <sub>θJA</sub>	35	°C/W	

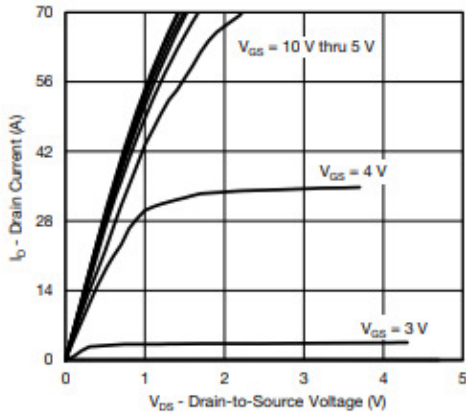
### Electrical Characteristics

(T<sub>A</sub>=25°C Unless otherwise noted)

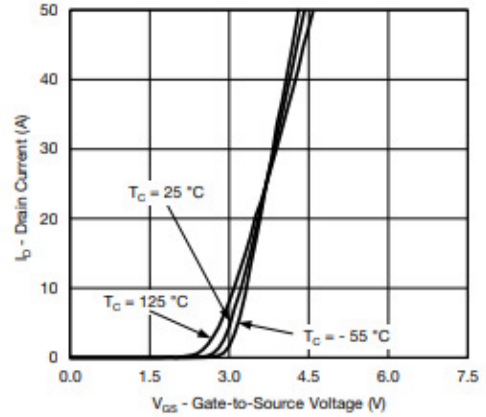
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	100			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1.0		2.5	
Gate Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V			1	uA
		V <sub>DS</sub> =80V, V <sub>GS</sub> =0V T <sub>J</sub> =85°C			5	
On-State Drain Current	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ 5V, V <sub>GS</sub> =10V	30			A
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =12A		18	22	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A		24	30	
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =10A		26		S
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =4.0A, V <sub>GS</sub> =0V		0.85	1.3	V
<b>Dynamic</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =4.5V I <sub>D</sub> ≡10A		10	20	nC
Gate-Source Charge	Q <sub>gs</sub>			2.8		
Gate-Drain Charge	Q <sub>gd</sub>			4.6		
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V f=1MHz		900		pF
Output Capacitance	C <sub>oss</sub>			350		
Reverse Transfer Capacitance	C <sub>rss</sub>			35		
Turn-On Time	t <sub>d(on)</sub>	V <sub>DD</sub> =50V, R <sub>L</sub> =5Ω I <sub>D</sub> ≡10A, V <sub>GEN</sub> =10V R <sub>G</sub> =1Ω		12	25	ns
	t <sub>r</sub>			10	20	
Turn-Off Time	t <sub>d(off)</sub>			20	40	
	t <sub>f</sub>			10	20	



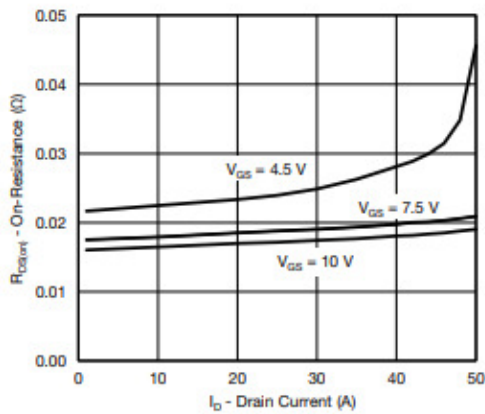
## Typical Characteristics



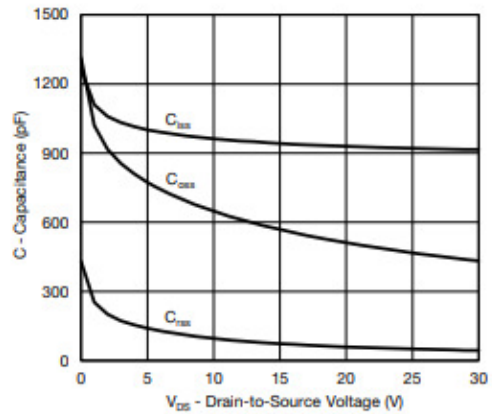
Output Characteristics



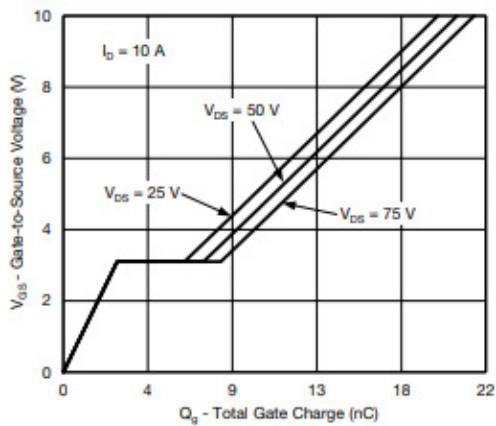
Transfer Characteristics



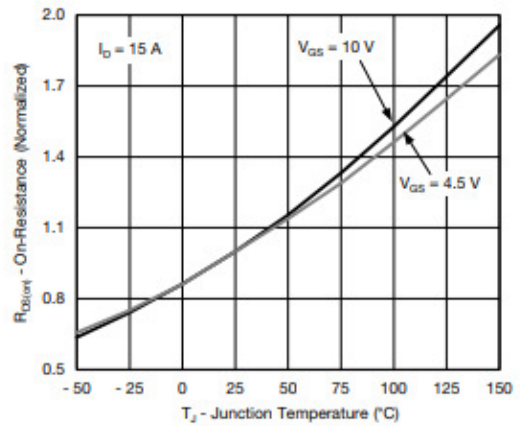
On-Resistance vs. Drain Current and Gate Voltage



Capacitance



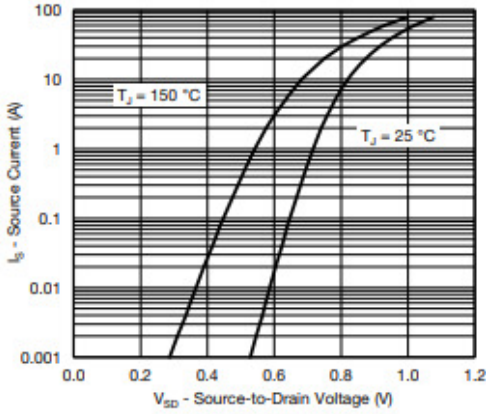
Gate Charge



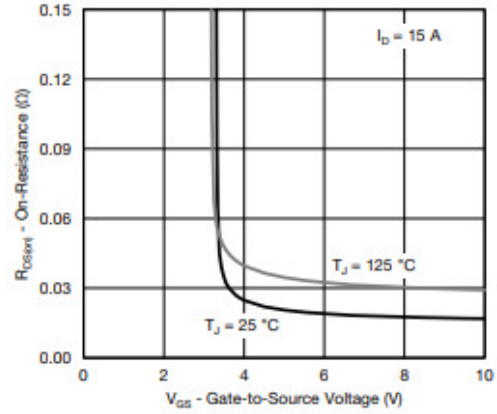
On-Resistance vs. Junction Temperature



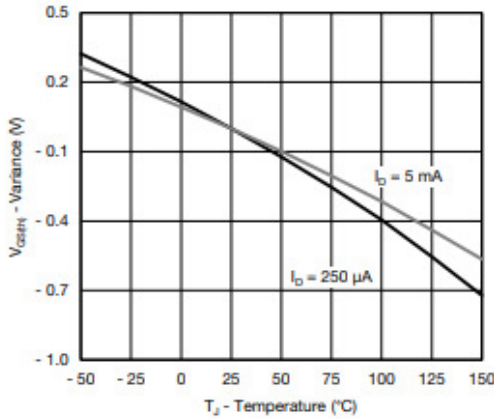
## Typical Characteristics



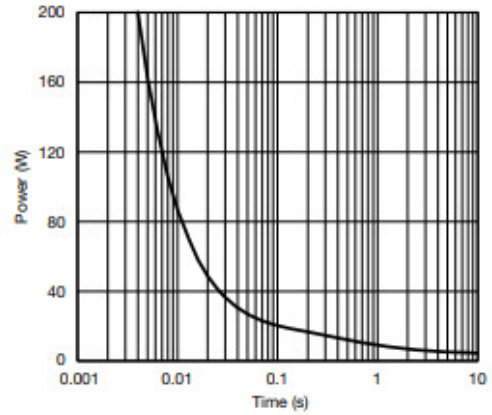
Source-Drain Diode Forward Voltage



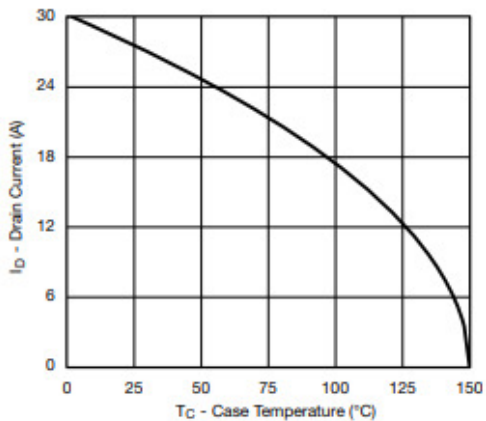
On-Resistance vs. Gate-to-Source Voltage



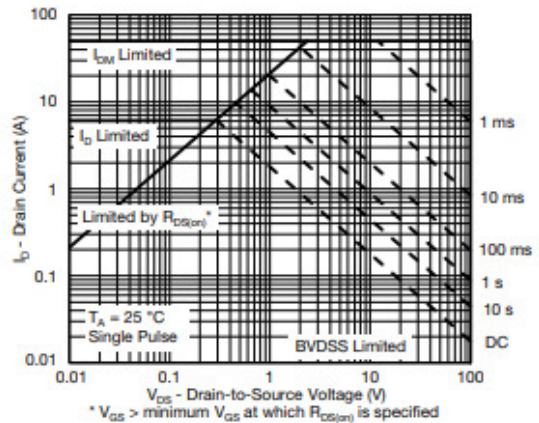
Threshold Voltage



Single Pulse Power, Junction-to-Ambient



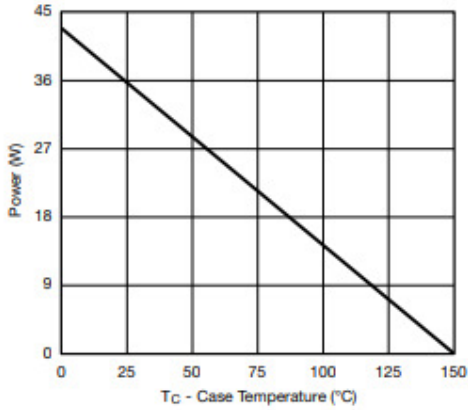
Current Derating <sup>a</sup>



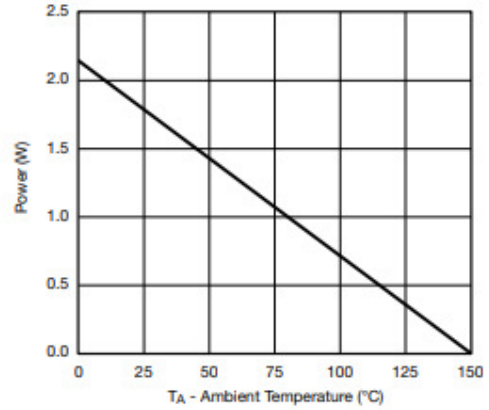
Safe Operating Area, Junction-to-Ambient



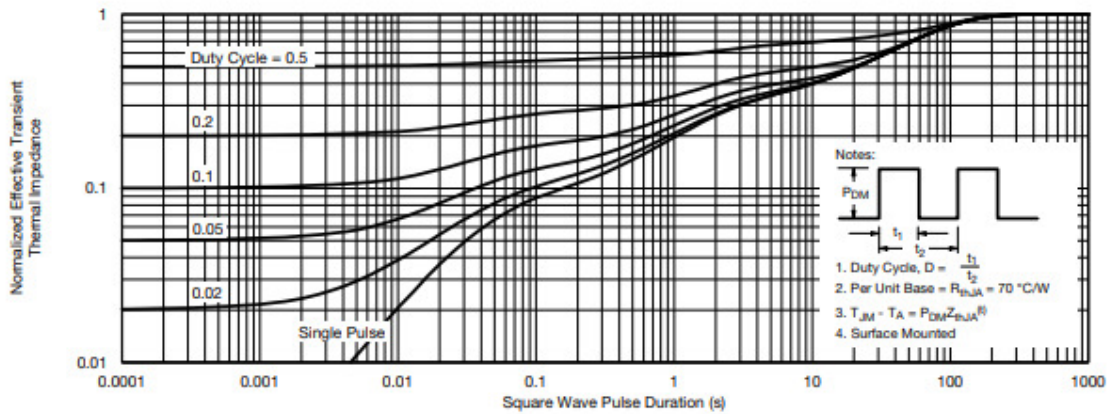
## Typical Characteristics



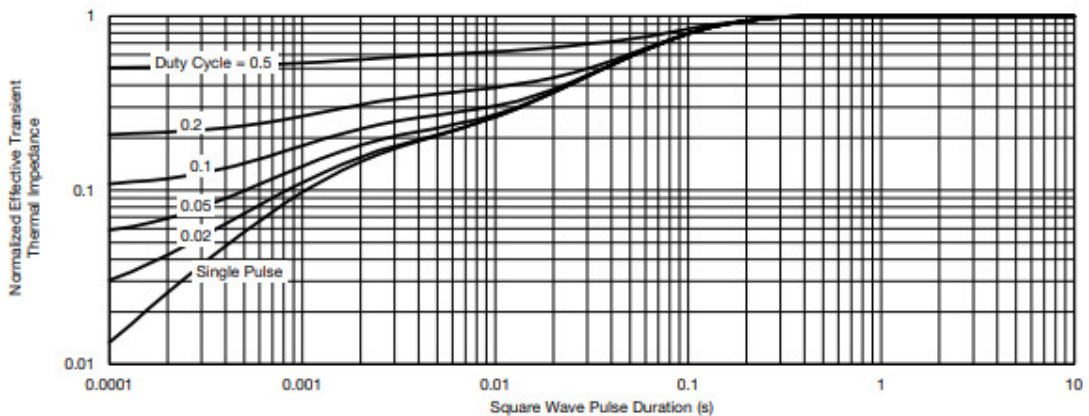
Power, Junction-to-Case



Power, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient

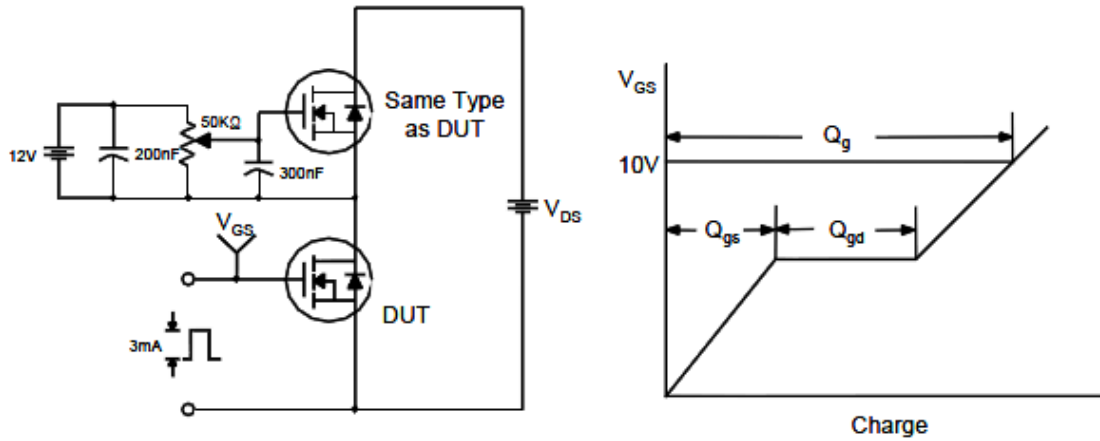


Normalized Thermal Transient Impedance, Junction-to-Case

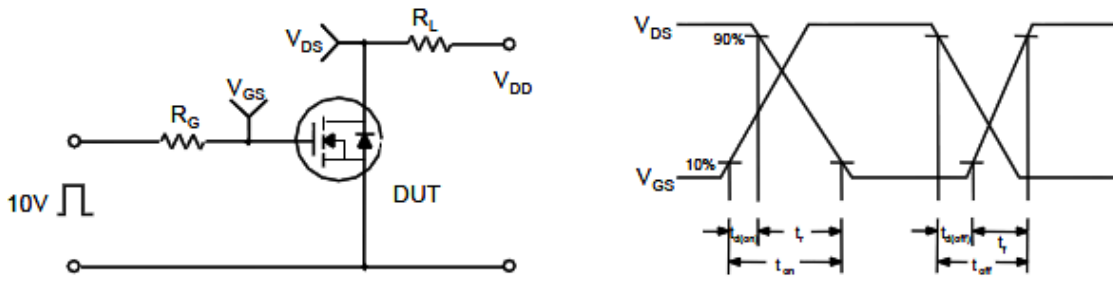


**Typical Characteristics**

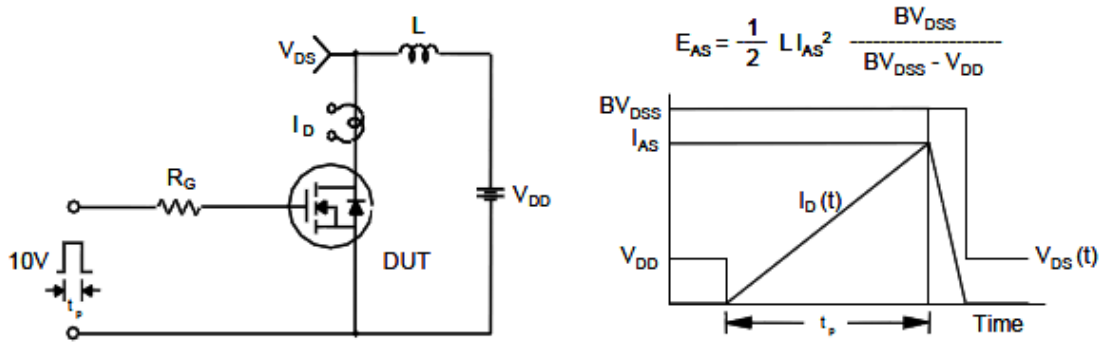
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

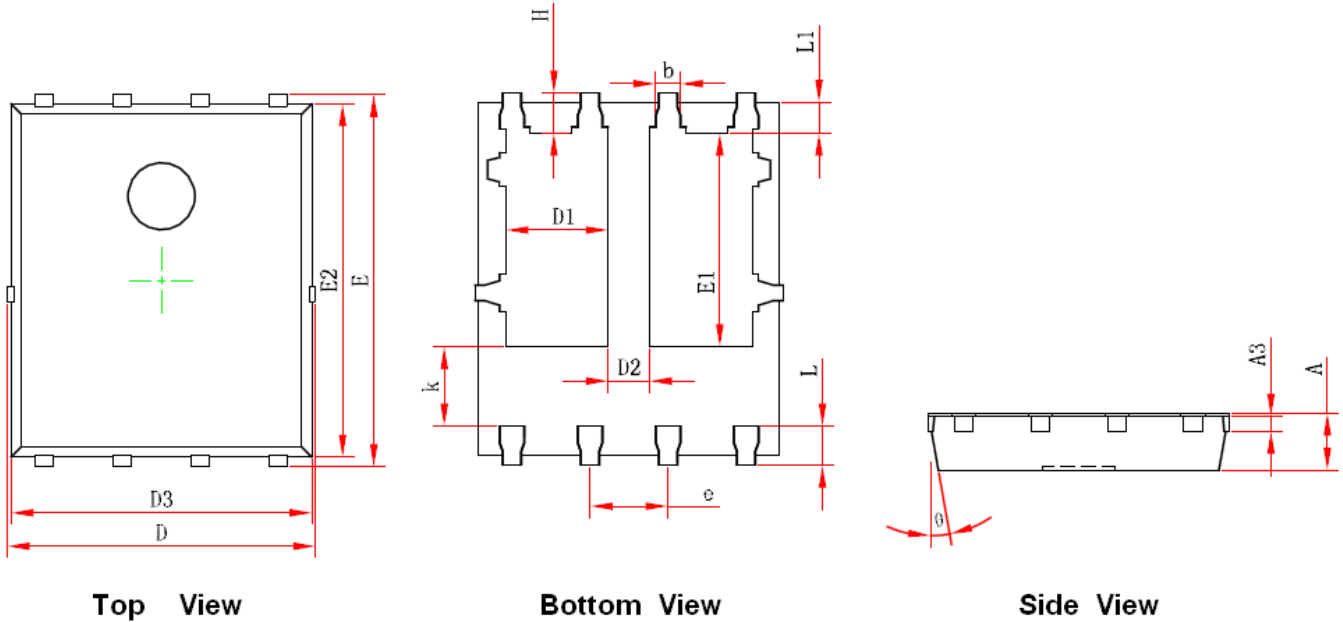


Unclamped Inductive Switching Test Circuit & Waveforms





**Package Information ( DFN 5X6-8L )**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039
A3	0.254 REF.		0.010REF.	
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	1.470	1.870	0.058	0.074
D2	0.470	0.870	0.019	0.034
E1	3.375	3.575	0.133	0.141
D3	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
$\theta$	10°	12°	10°	12°

©2010 Alfa-MOS Technology Corp.  
 2F, No.80, Sec.1, Cheng Kung Rd., Nan Kang Dist., Taipei City 115, Taiwan (R.O.C.)  
 Tel : 886 2) 2651 3928  
 Fax : 886 2) 2786 8483  
 ©http://www.alfa-mos.com