



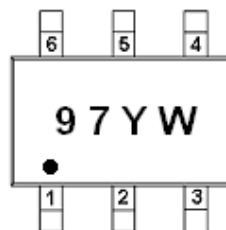
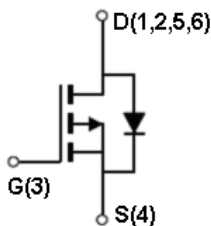
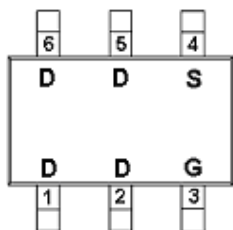
General Description

AFP3497, P-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent $R_{DS(ON)}$, low gate charge. These devices are particularly suited for low voltage power management, such as smart phone and notebook computer and other battery powered circuits, and low in-line power loss are needed in commercial industrial surface mount applications.

Features

- -20V/-3.8A, $R_{DS(ON)}=100m\Omega@V_{GS}=-4.5V$
- -20V/-2.6A, $R_{DS(ON)}=140m\Omega@V_{GS}=-2.5V$
- -20V/-1.5A, $R_{DS(ON)}=190m\Omega@V_{GS}=-1.8V$
- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- TSOP-6 package design

Pin Description (TSOP-6)



Application

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- Net Working System

Pin Define

Pin	Symbol	Description
1	G	Gate
2	S	Source
3	D	Drain

Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFP3497TS6RG	97YW	TSOP-6	Tape & Reel	3000 EA

- ※ 97 parts code
- ※ Y year code (0 ~ 9)
- ※ W week code (A ~ Z = 1 ~ 26 / a ~ z = 27 ~ 52)
- ※ AFP3497TS6RG : 7" Tape & Reel ; Pb- Free ; Halogen -Free



Absolute Maximum Ratings

($T_A=25^\circ\text{C}$ Unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	-20	V
Gate-Source Voltage	V_{GSS}	± 12	V
Continuous Drain Current($T_J=150^\circ\text{C}$)	I_D	$T_A=25^\circ\text{C}$	-3.8
		$T_A=70^\circ\text{C}$	-2.6
Pulsed Drain Current	I_{DM}	-20	A
Continuous Source Current(Diode Conduction)	I_S	-1.7	A
Power Dissipation	P_D	$T_A=25^\circ\text{C}$	2.0
		$T_A=70^\circ\text{C}$	1.3
Operating Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55/150	$^\circ\text{C}$
Thermal Resistance-Junction to Ambient	$R_{\theta JA}$	120	$^\circ\text{C/W}$

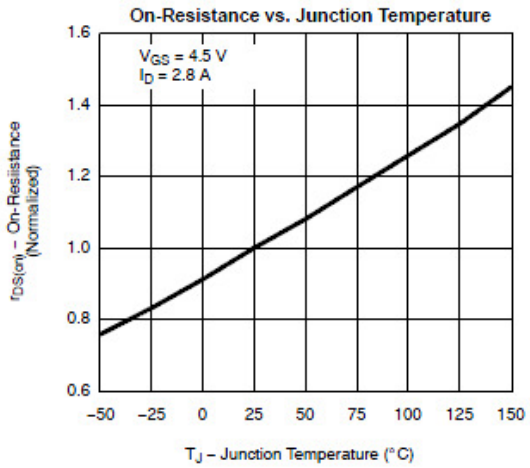
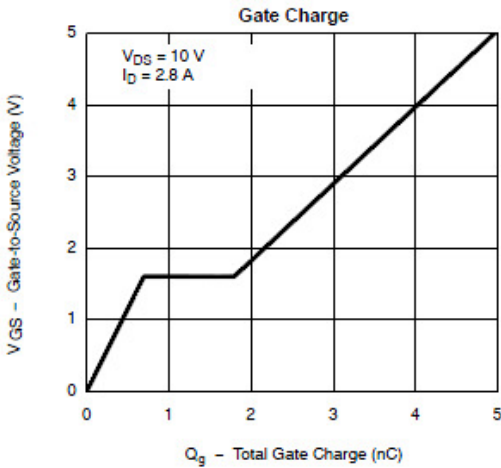
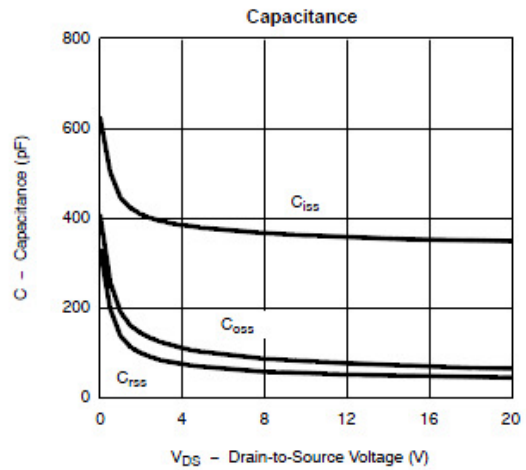
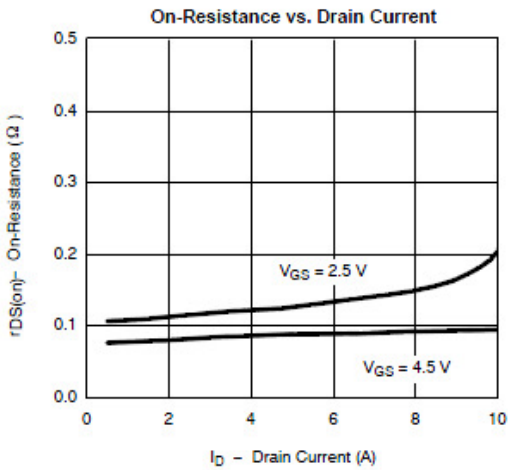
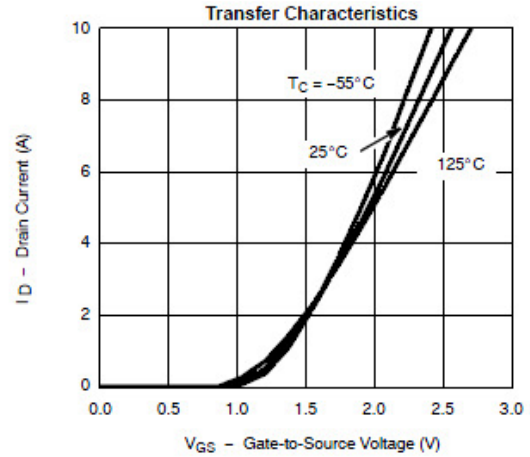
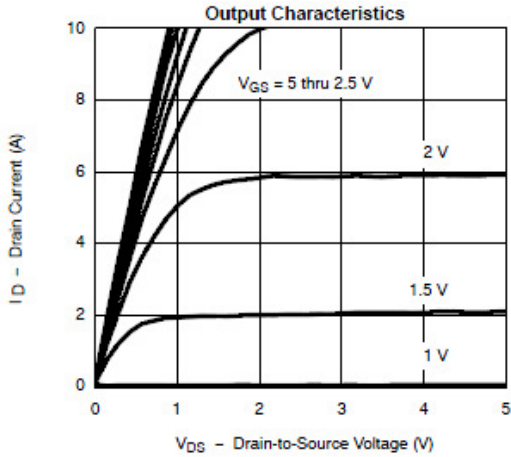
Electrical Characteristics

($T_A=25^\circ\text{C}$ Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=-250\mu\text{A}$	-20			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu\text{A}$	-0.3		-0.7	
Gate Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 12V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-16V, V_{GS}=0V$			-1	μA
		$V_{DS}=-16V, V_{GS}=0V$ $T_J=85^\circ\text{C}$			-30	
On-State Drain Current	$I_{D(on)}$	$V_{DS} \leq -5V, V_{GS}=-4.5V$	-6			A
		$V_{DS} \leq -5V, V_{GS}=-2.5V$	-3			
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=-4.5V, I_D=-3.8A$		90	100	m Ω
		$V_{GS}=-2.5V, I_D=-2.6A$		124	140	
		$V_{GS}=-1.8V, I_D=-1.5A$		170	190	
Forward Transconductance	g_{FS}	$V_{DS}=-5V, I_D=-2.8A$		6.5		S
Diode Forward Voltage	V_{SD}	$I_S=-1.25A, V_{GS}=0V$		-0.75	-1.3	V
Dynamic						
Total Gate Charge	Q_g	$V_{DS}=-6V, V_{GS}=-4.5V$ $I_D \equiv -2.8A$		5.8	10	nC
Gate-Source Charge	Q_{gs}			0.85		
Gate-Drain Charge	Q_{gd}			1.7		
Input Capacitance	C_{iss}	$V_{DS}=-6V, V_{GS}=0V$ $f=1\text{MHz}$		415		pF
Output Capacitance	C_{oss}			223		
Reverse Transfer Capacitance	C_{rss}			87		
Turn-On Time	$t_{d(on)}$	$V_{DD}=-6V, R_L=6\Omega$ $I_D \equiv -1.0A, V_{GEN}=-4.5V$ $R_G=6\Omega$		13	25	ns
	t_r			36	60	
Turn-Off Time	$t_{d(off)}$			42	70	
	t_f			34	60	

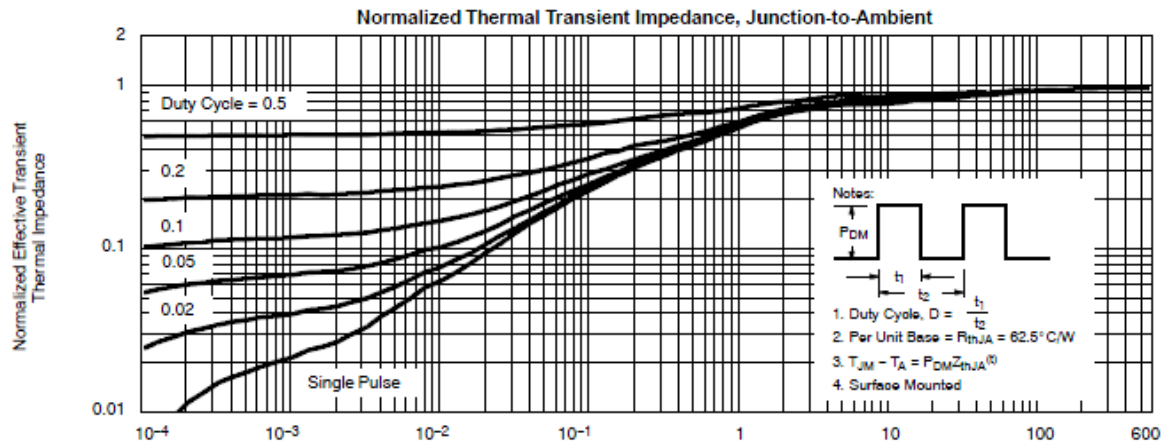
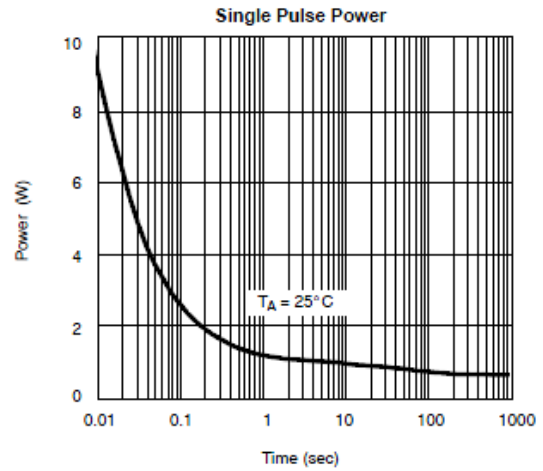
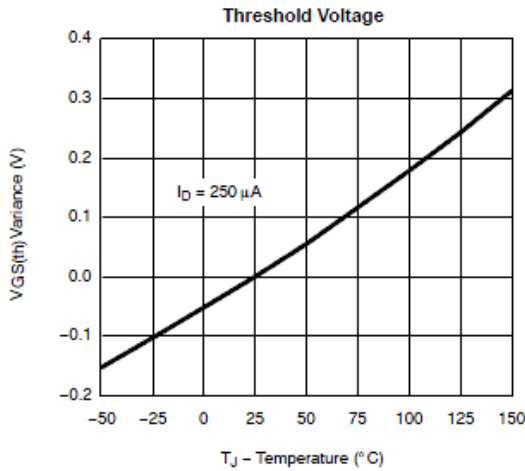
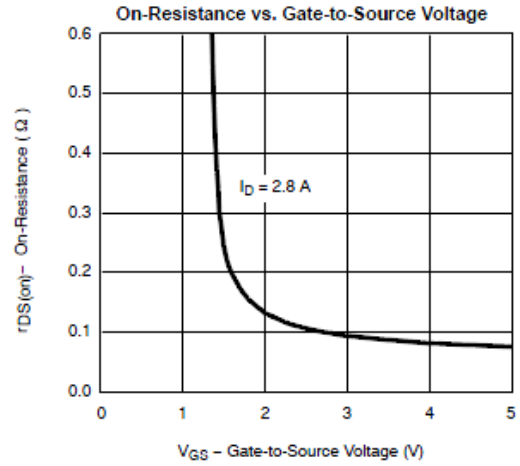
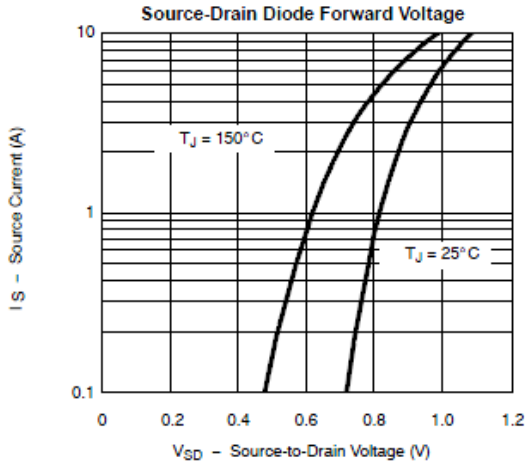


Typical Characteristics





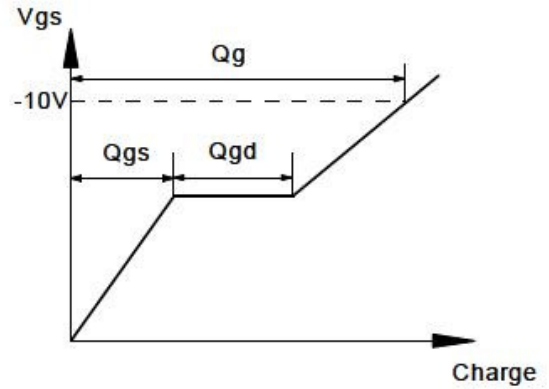
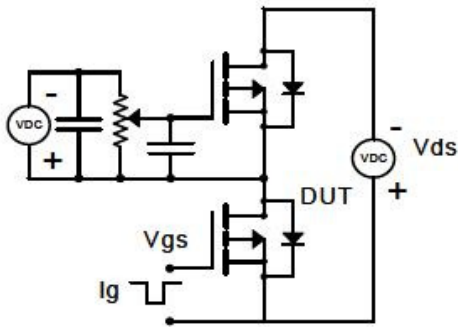
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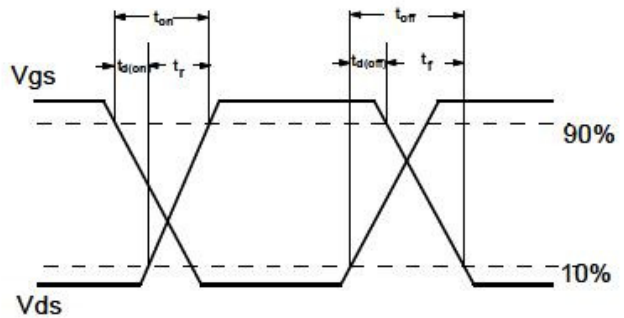
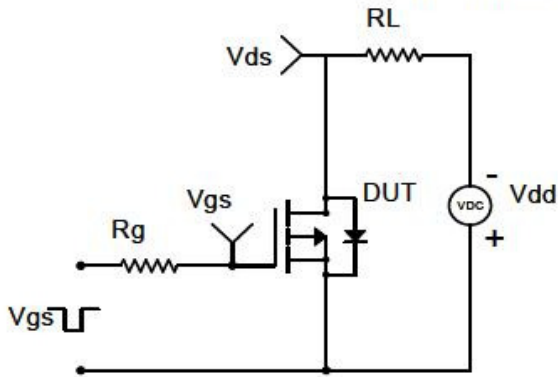


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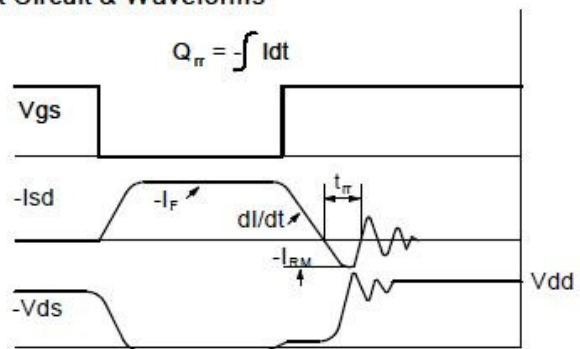
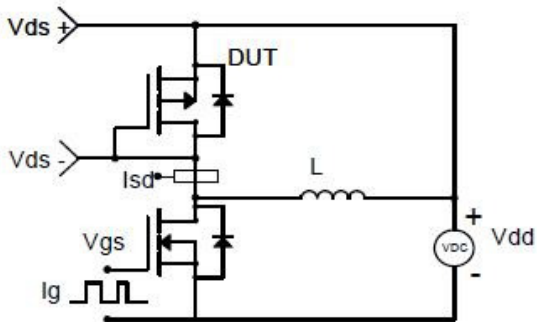
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

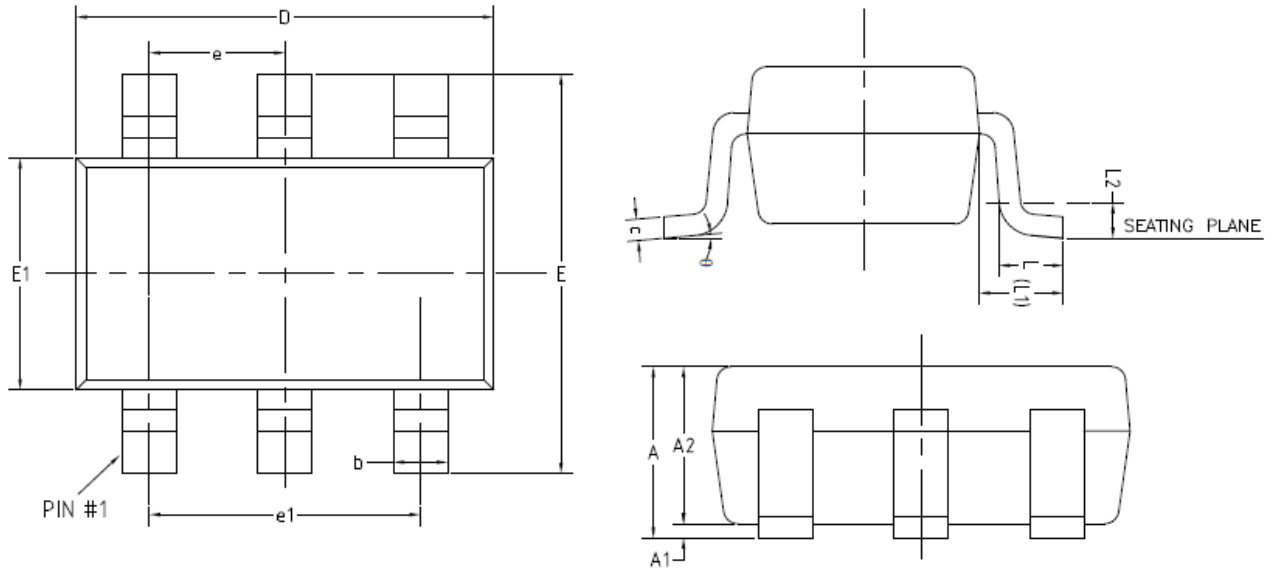


Diode Recovery Test Circuit & Waveforms





Package Information (TSOP-6)



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	0.70	—	0.90
A1	0	—	0.10
A2	0.70	0.75	0.80
b	0.35	—	0.50
c	0.08	—	0.20
D	2.82	2.92	3.02
E	2.65	2.80	2.95
E1	1.60	1.65	1.70
e	0.95(BSC)		
e1	1.90(BSC)		
L	0.30	0.45	0.60
L1	0.59REF		
L2	0.25BSC		
θ	0°	—	8°

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